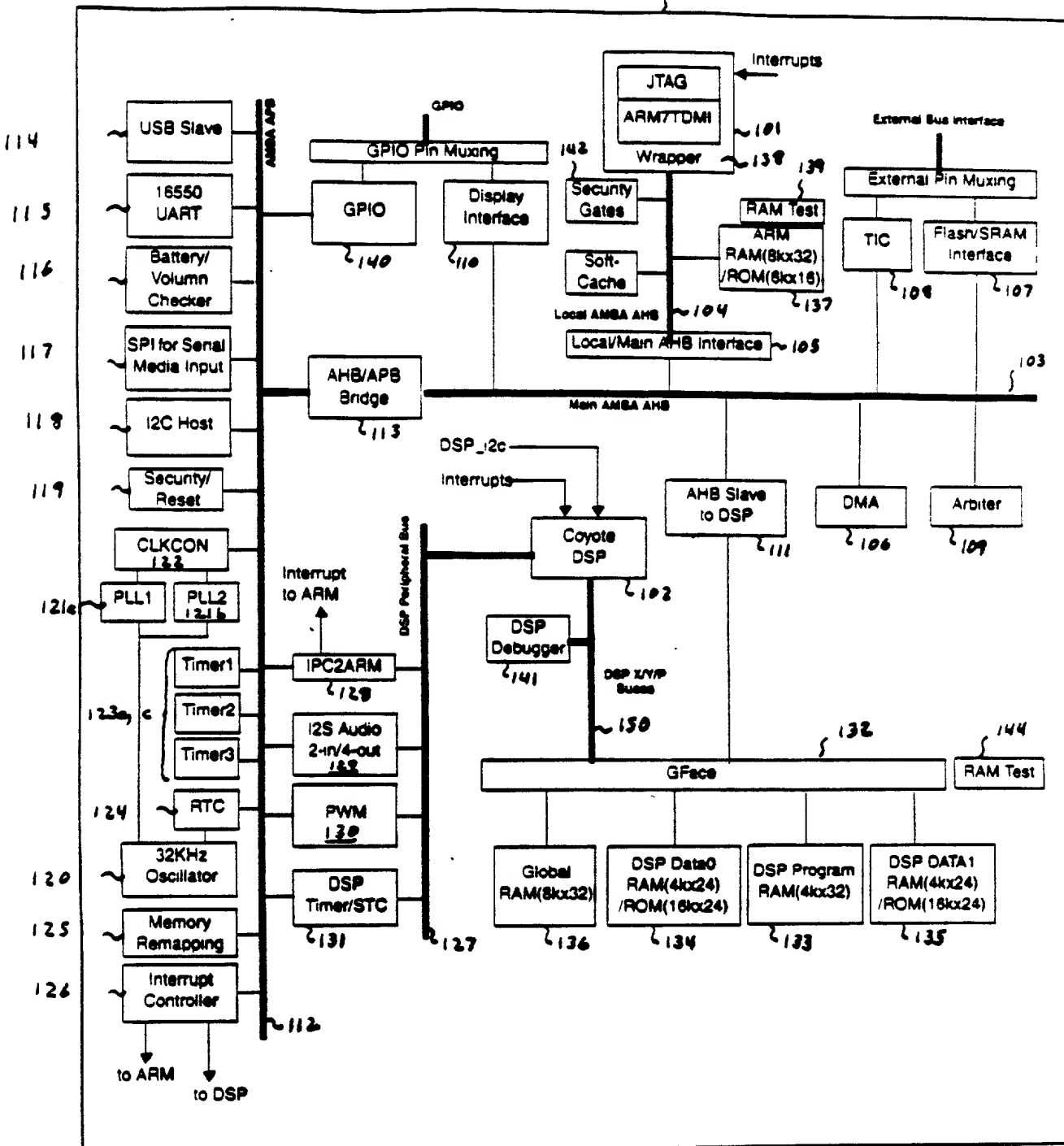
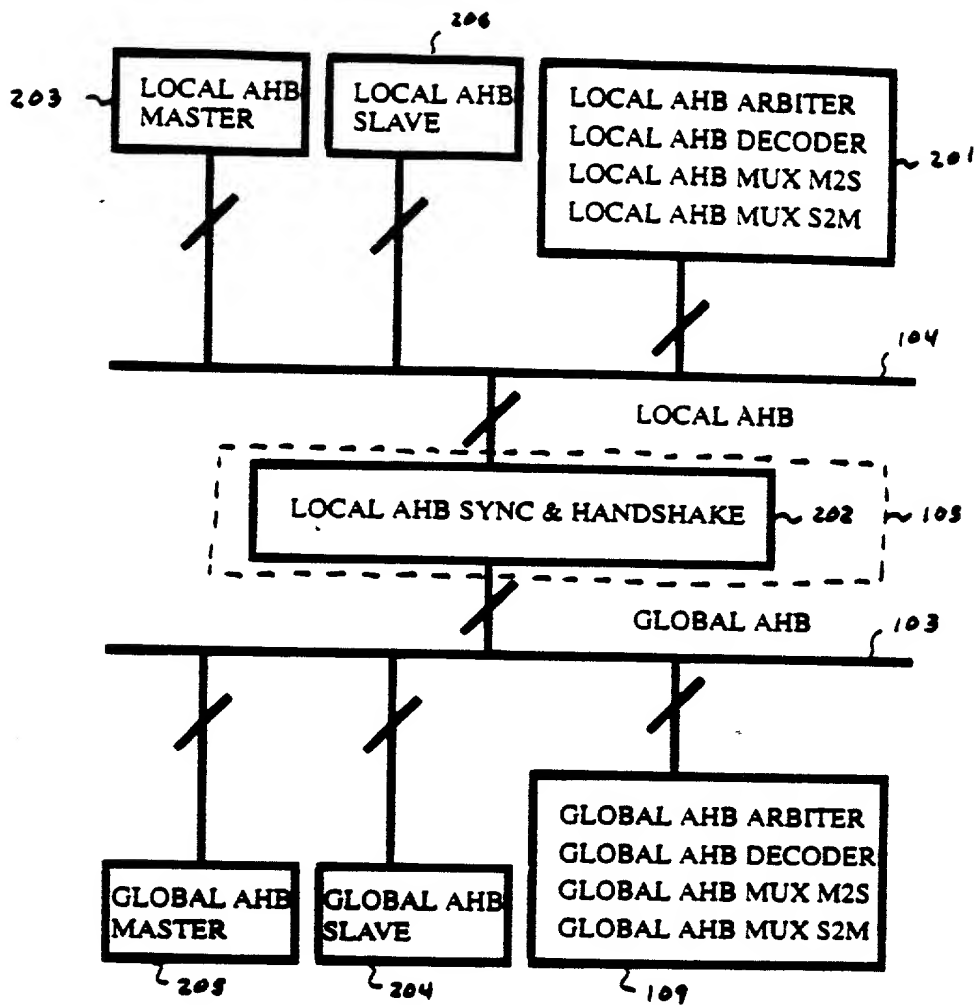


FIG. 1



2836-P1394S

FIG. 2A



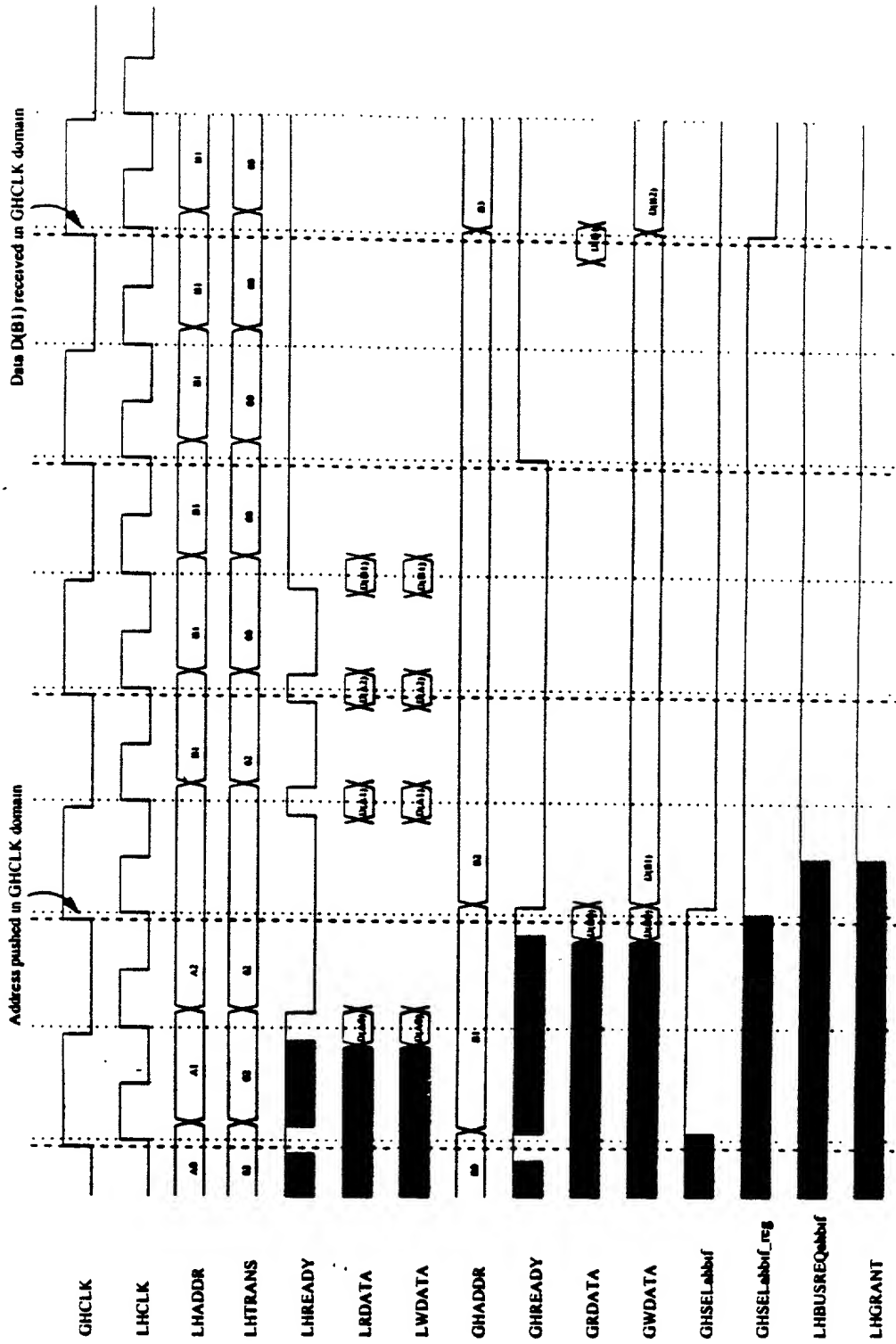


FIG. 2B

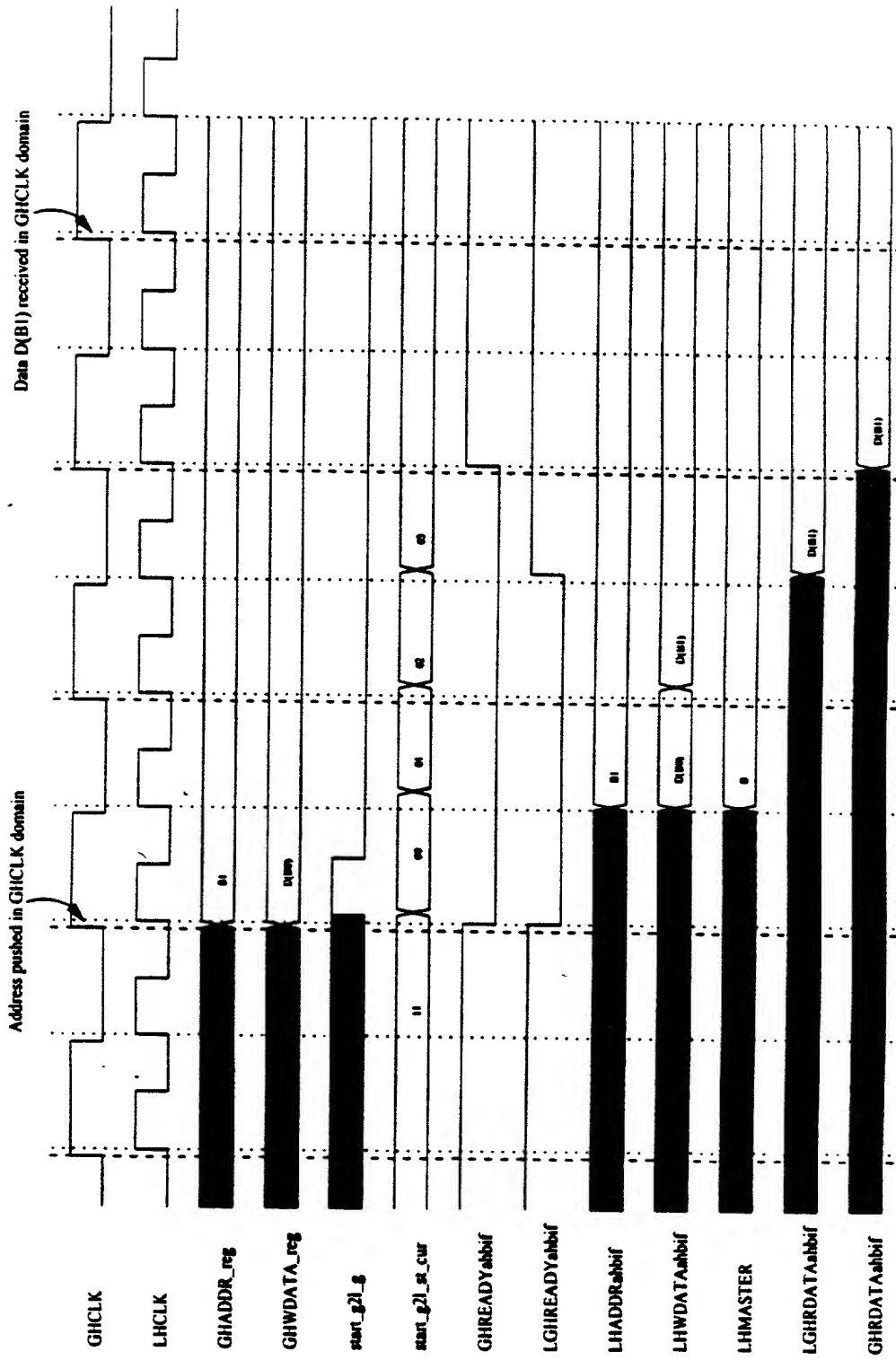


FIG 2C

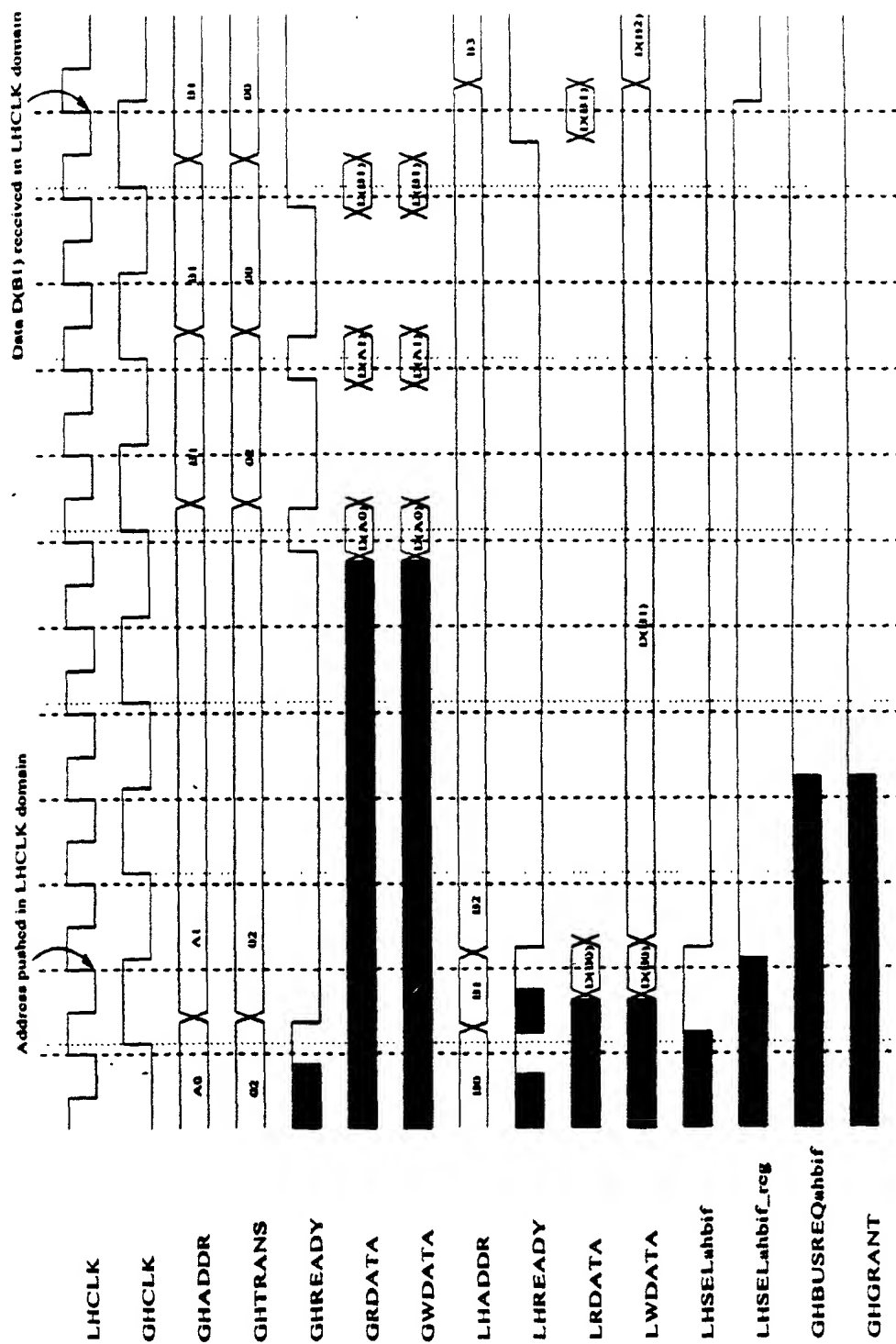


FIG. 2D

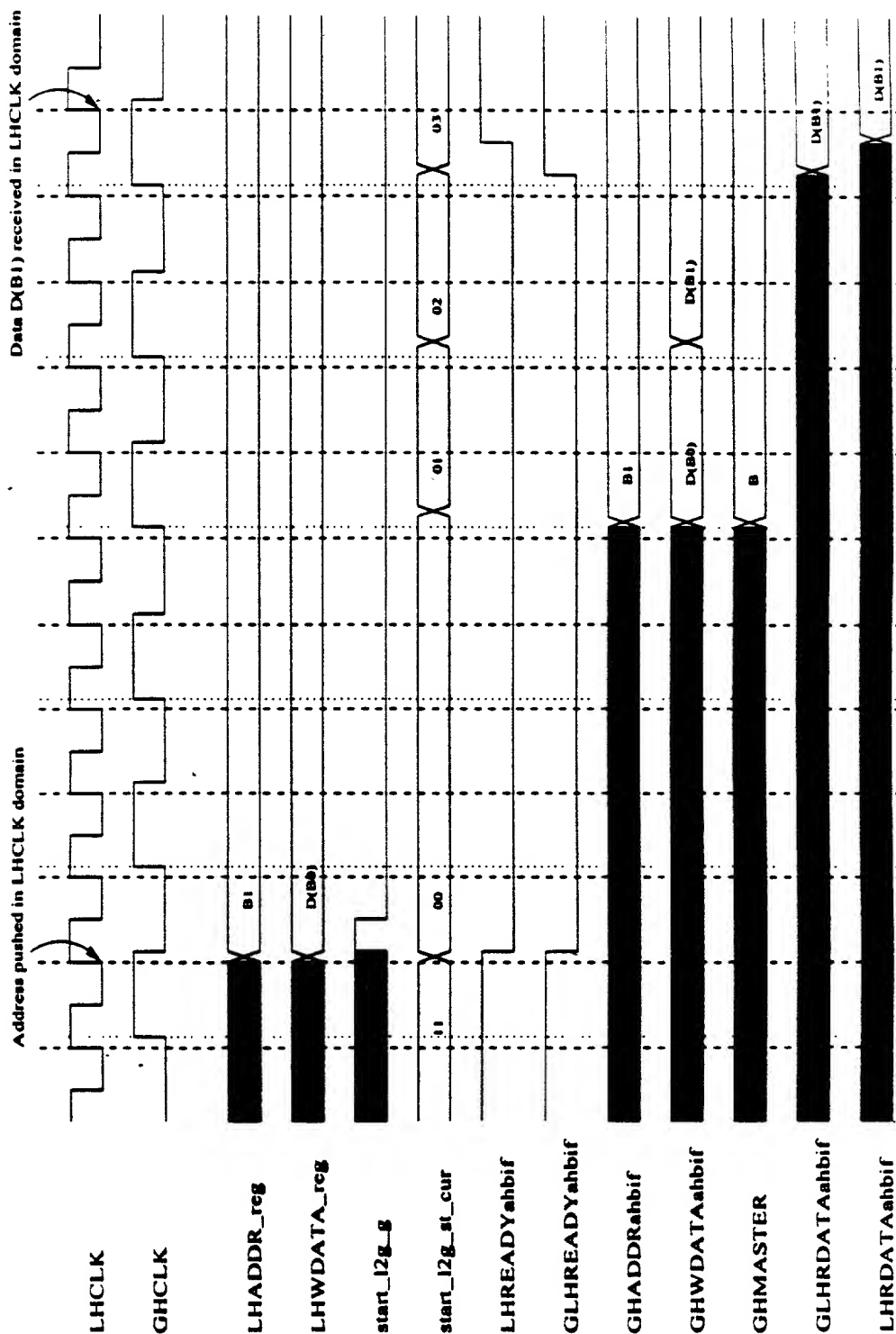
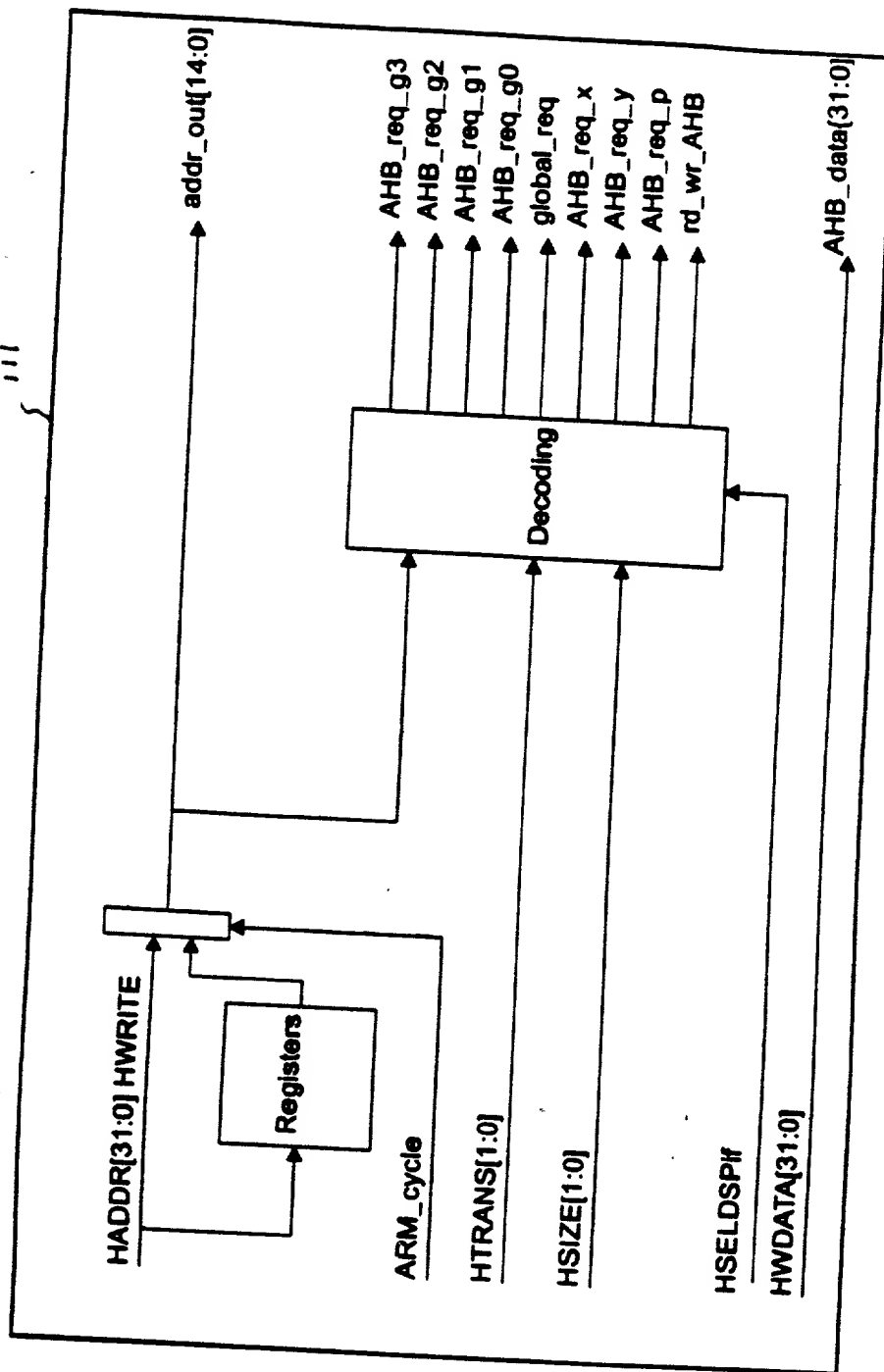


FIG. 2-16

FIG. 3



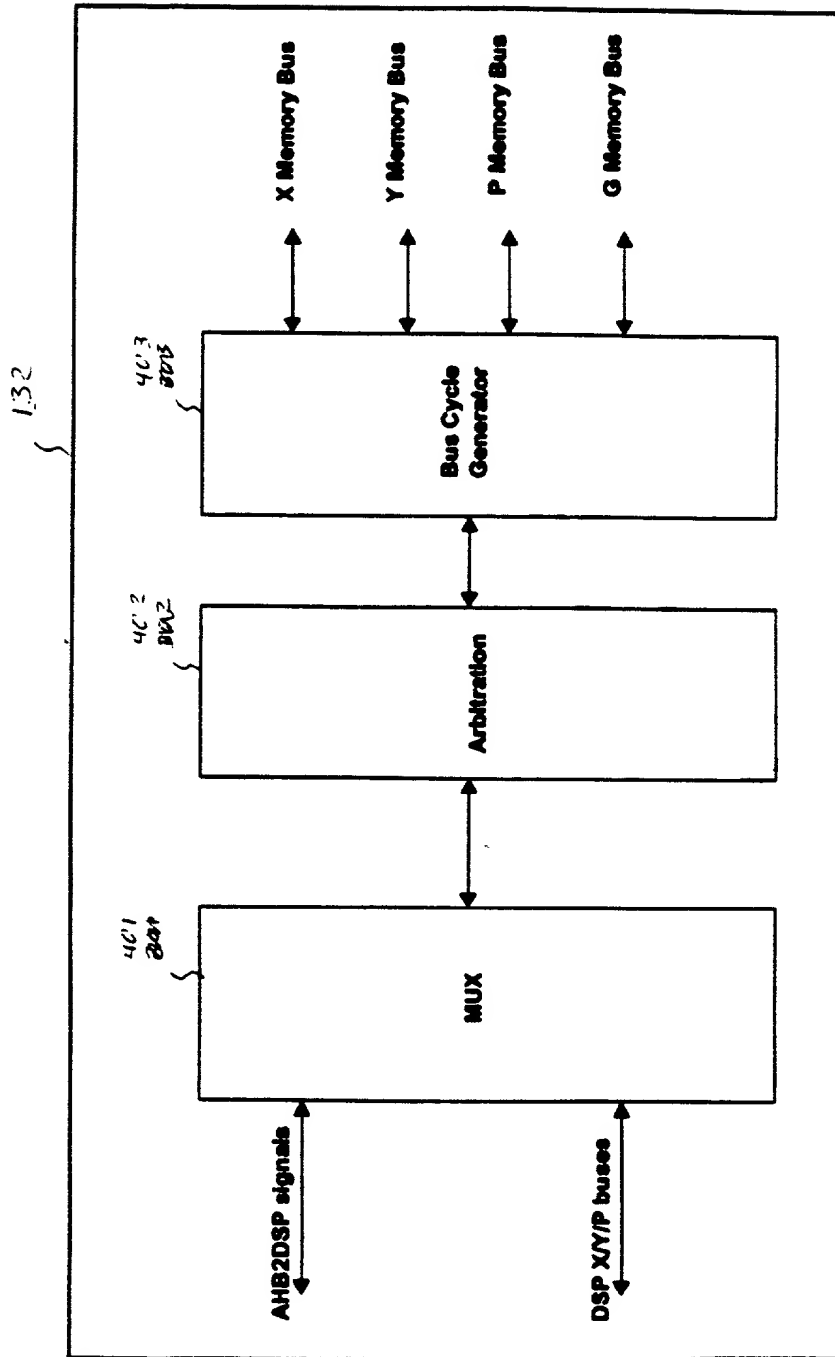


FIG. 4A

FIG. 4B

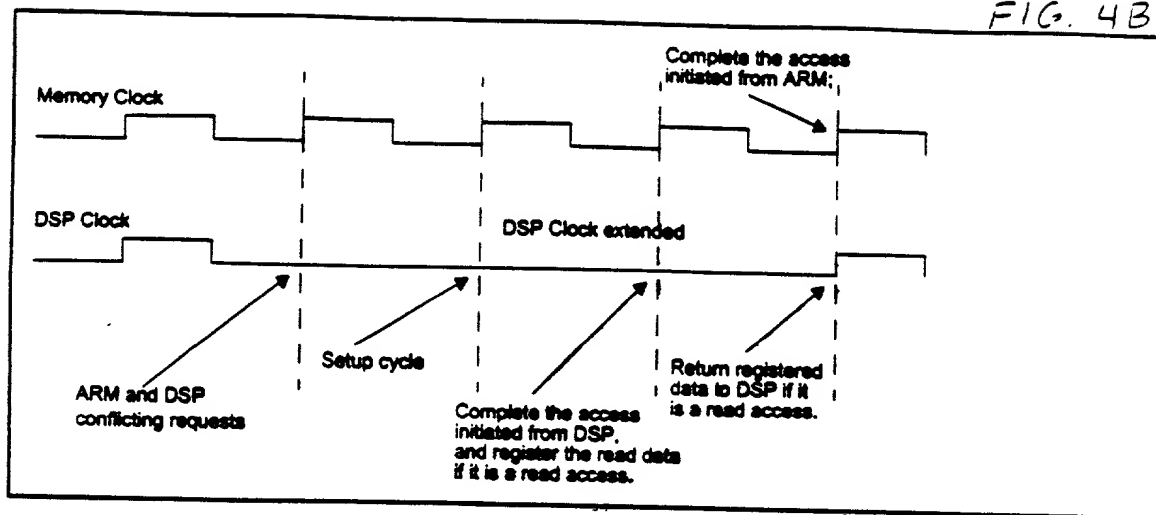
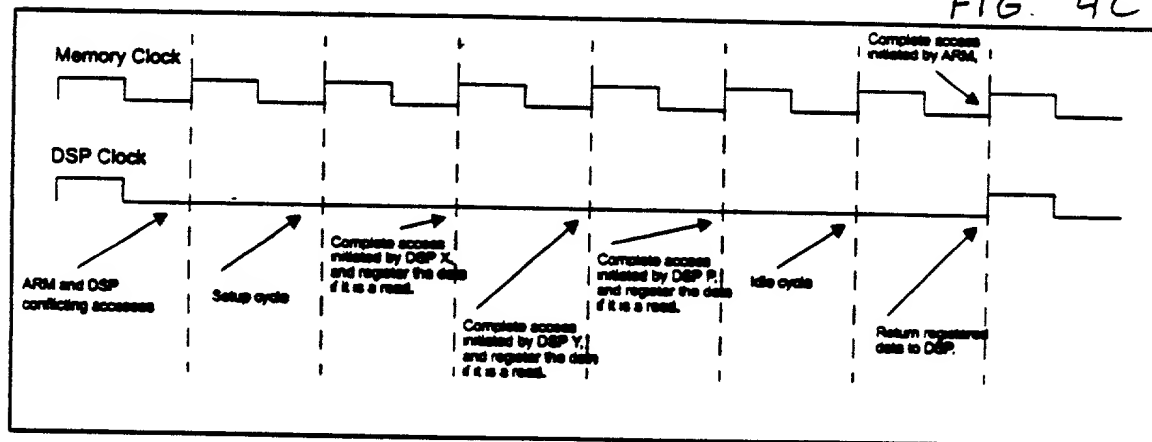
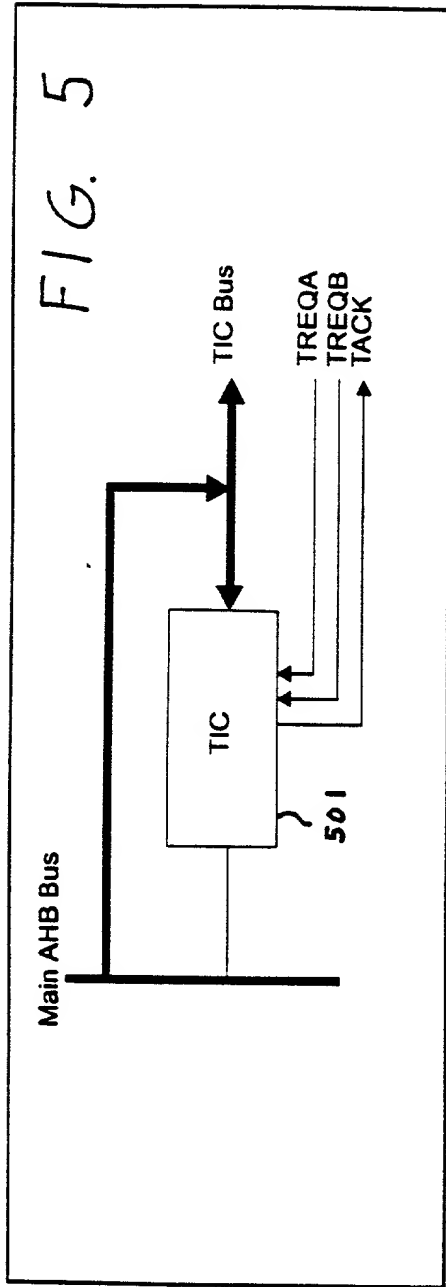
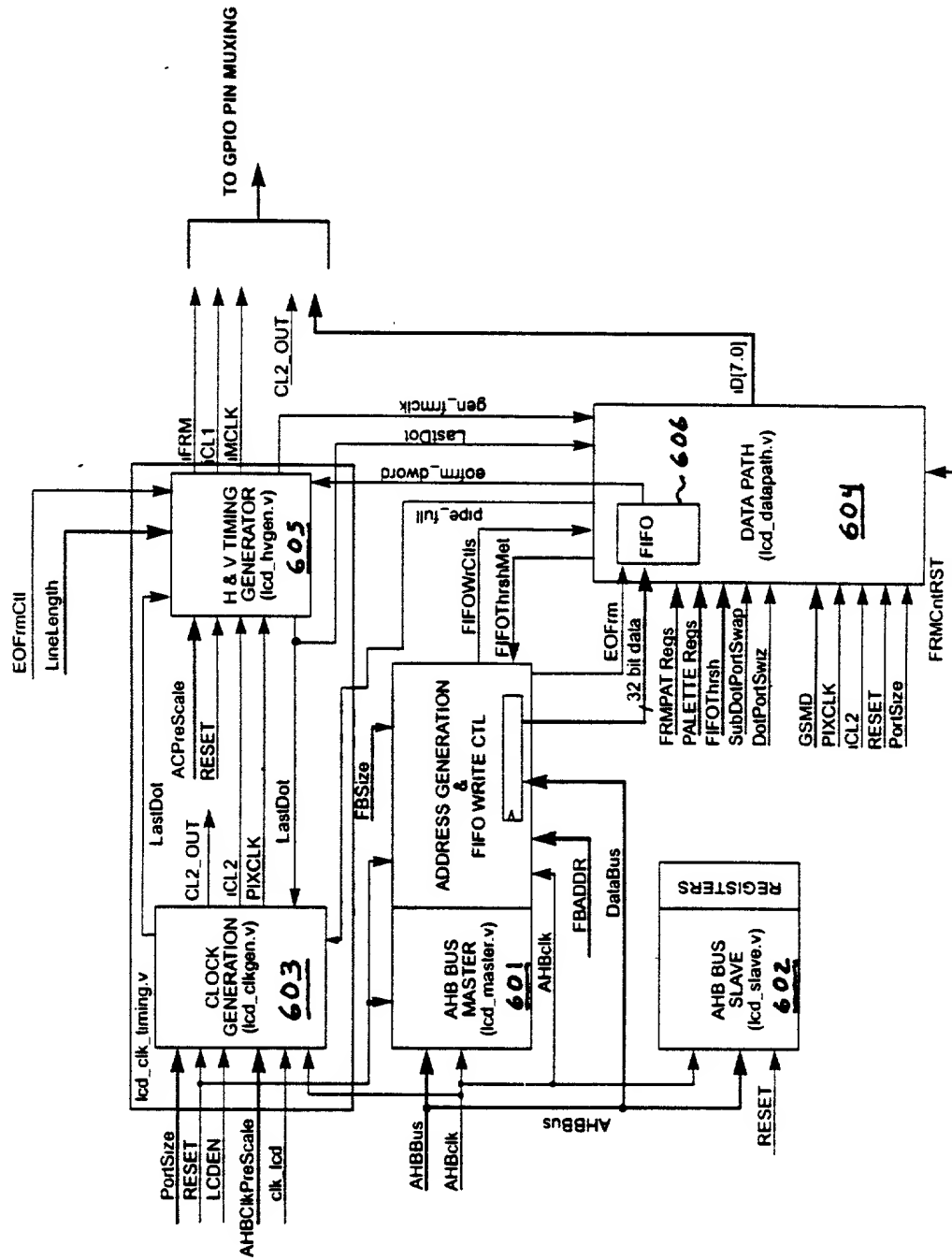


FIG. 4C







LCD Controller (lcd_top.v) Block Diagram

FIG. 6A

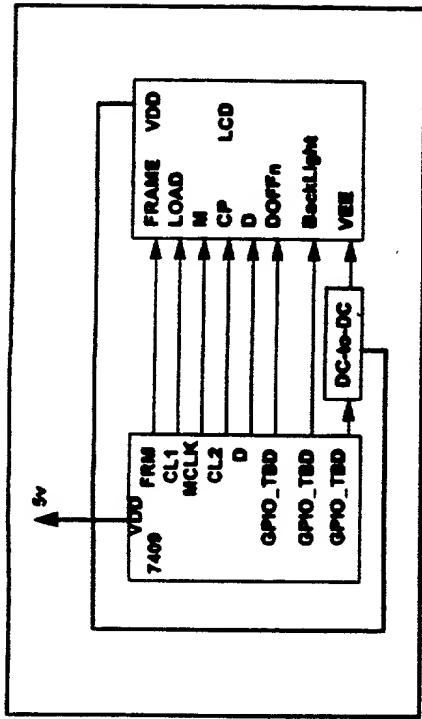


FIG 6B

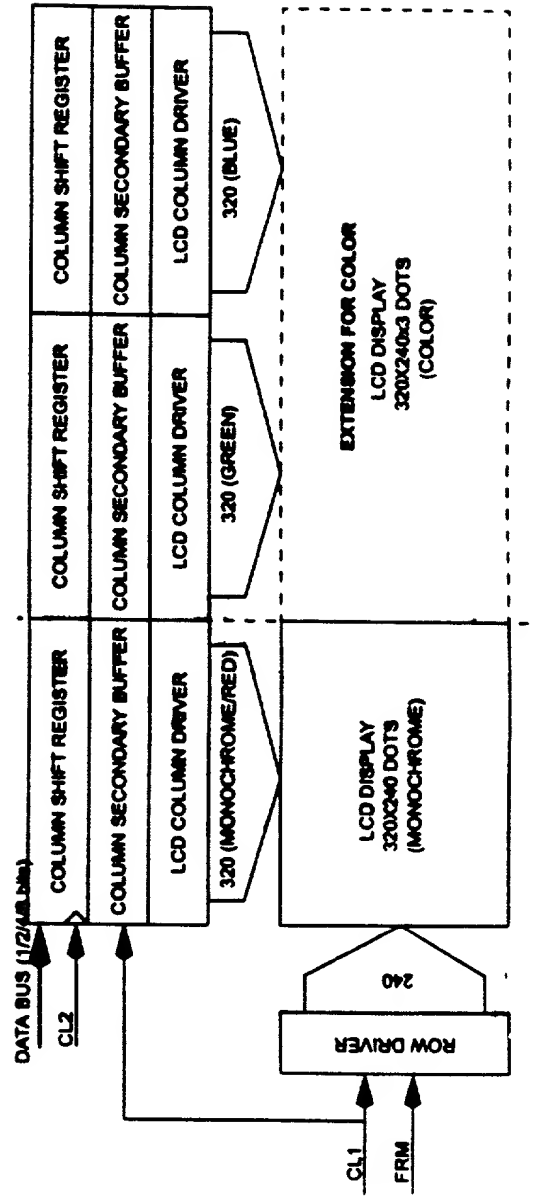


FIG. 6C

FIG 6 E

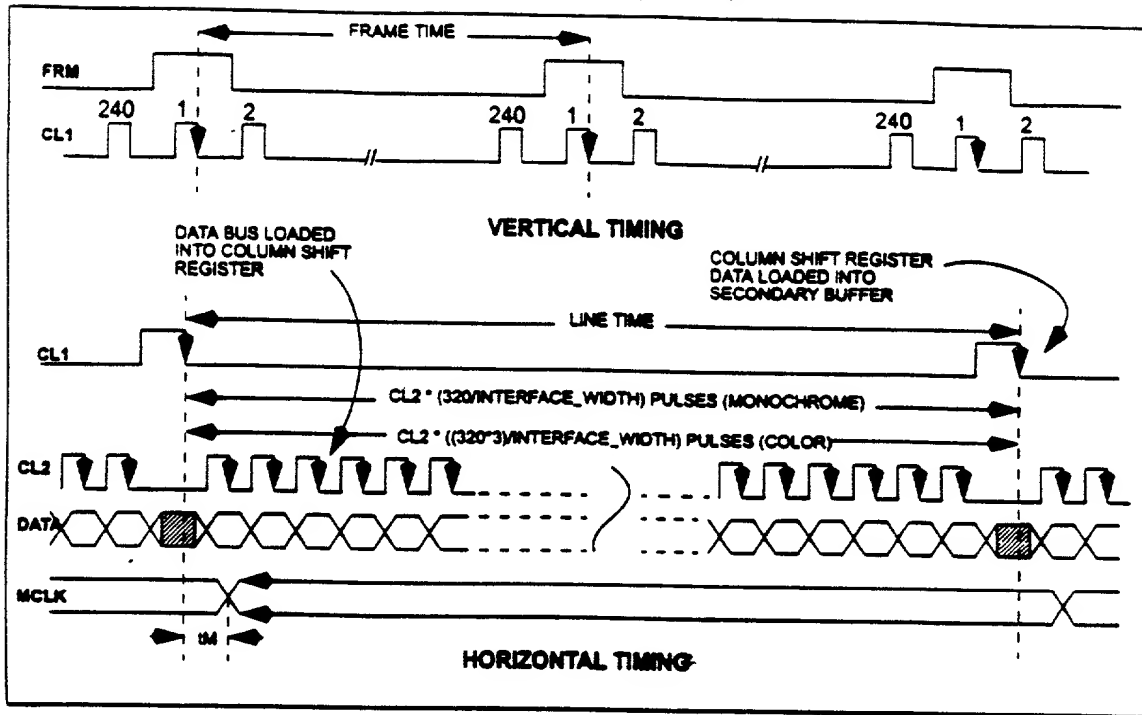
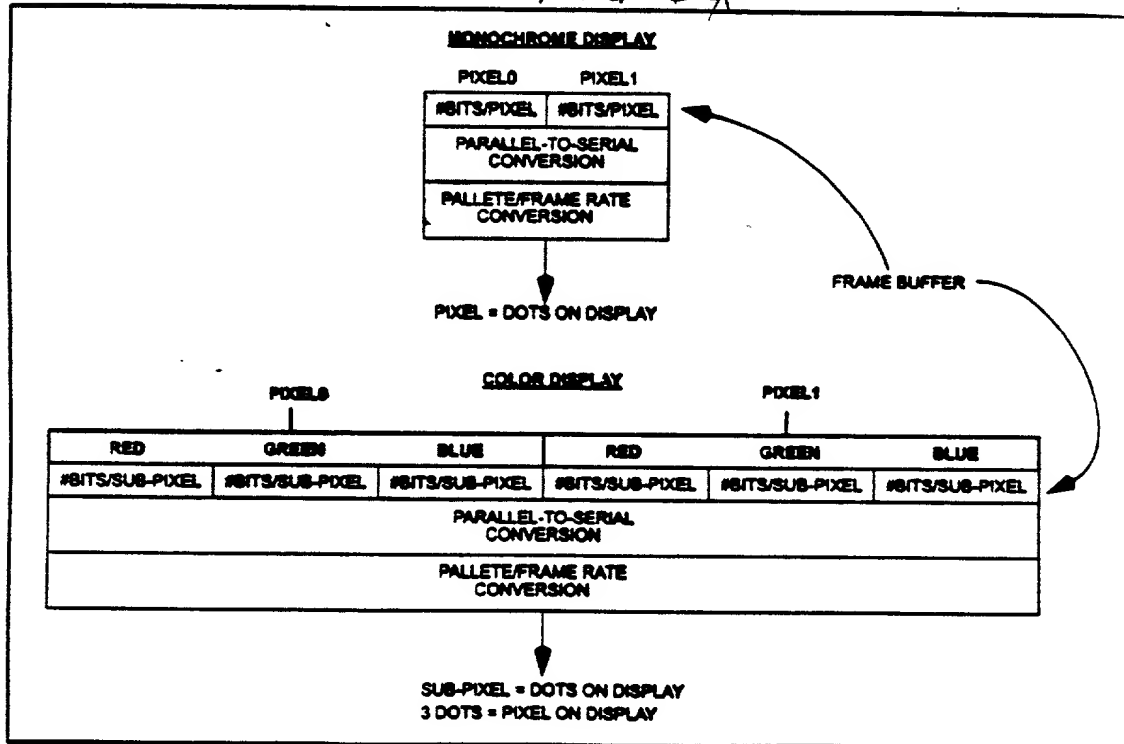


FIG 6 D



707

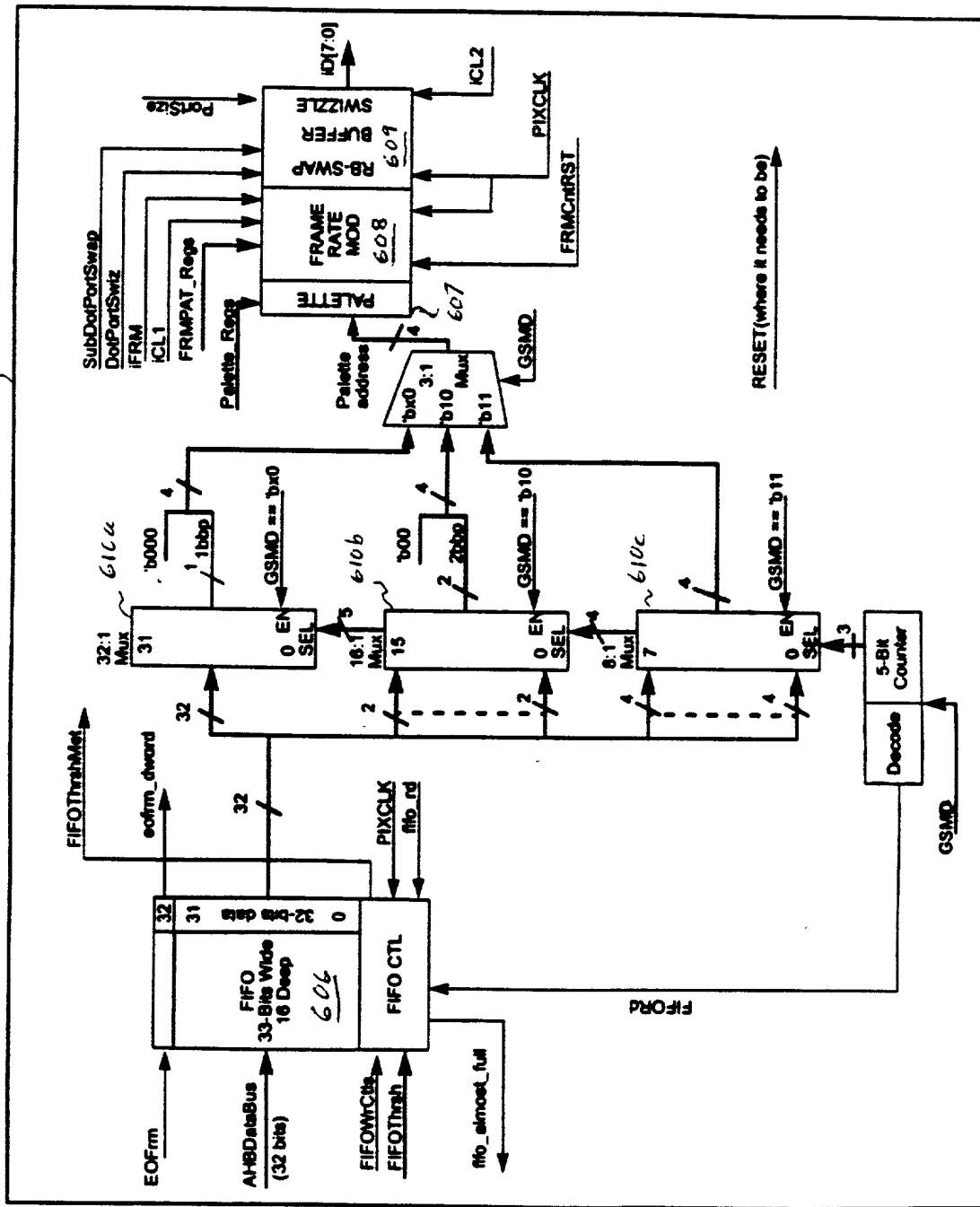


FIG 6 G

603

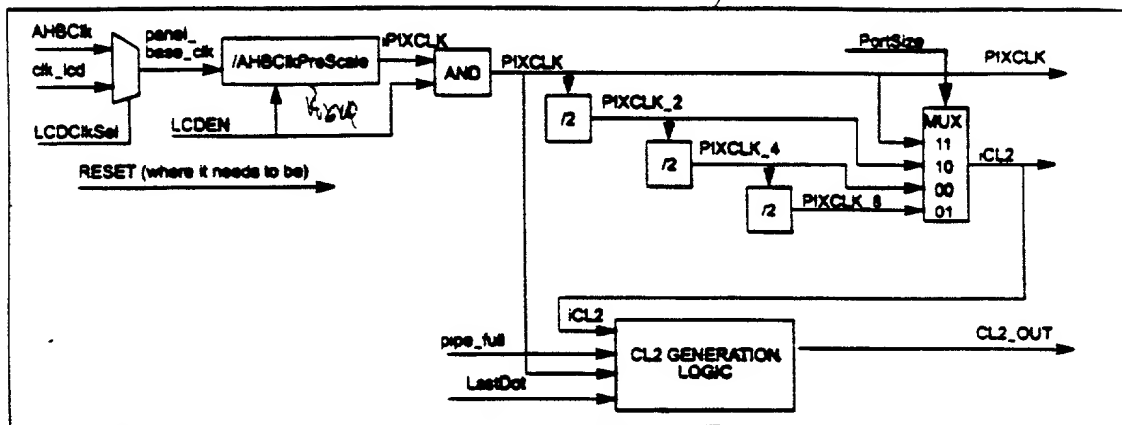


FIG 6 H

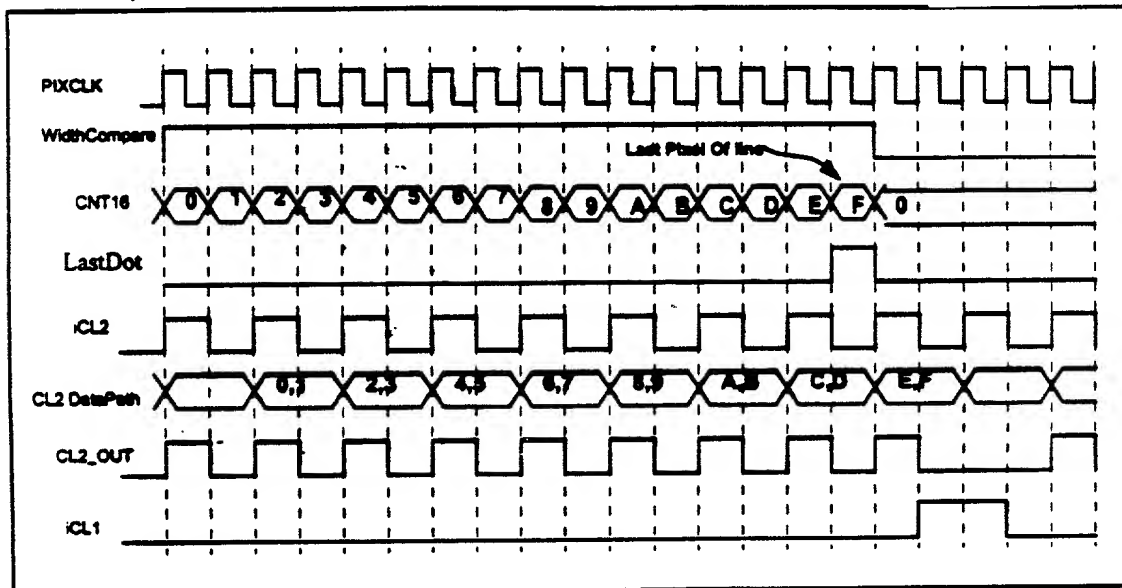


FIG 6I

605

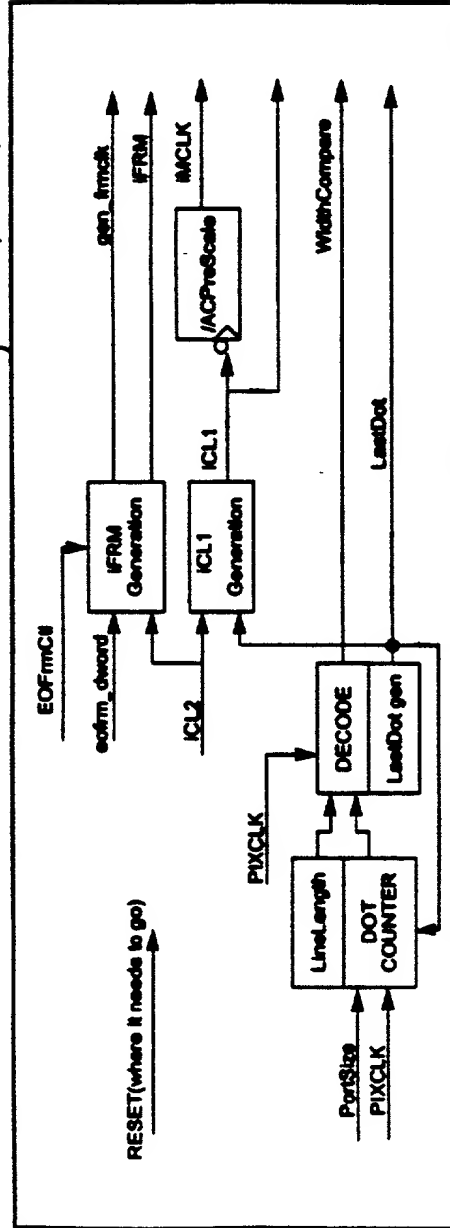


FIG 67

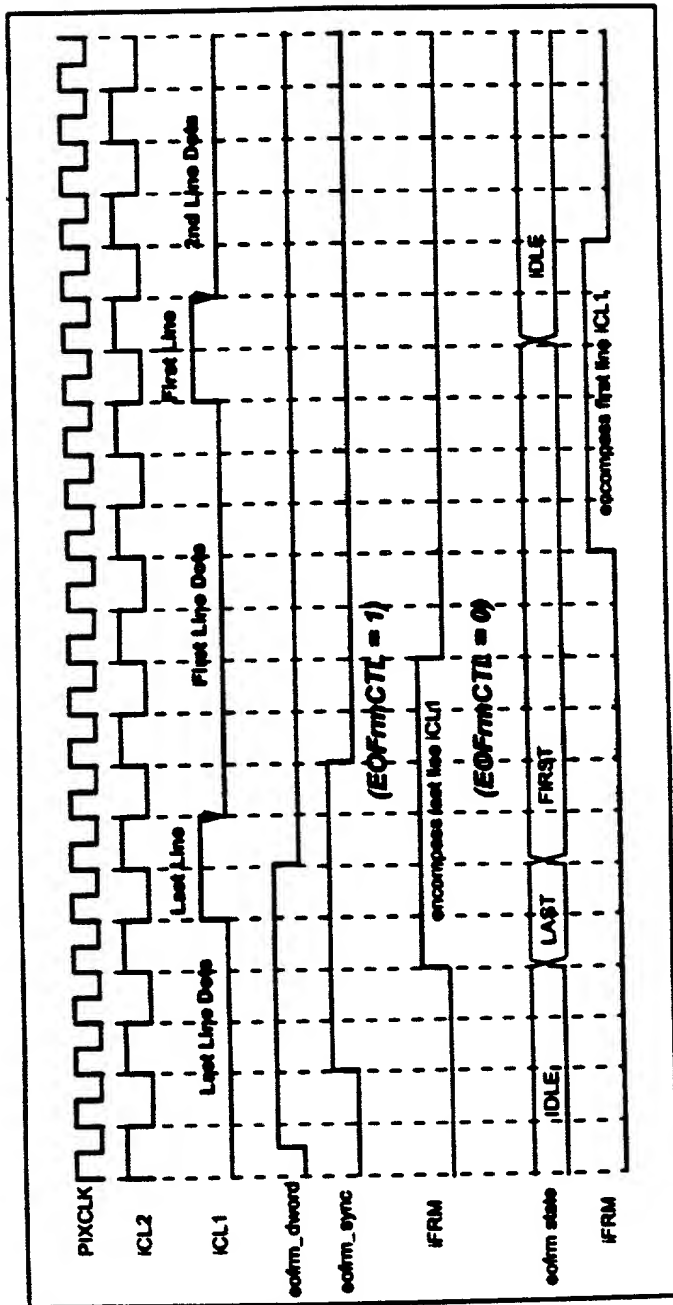


FIG. 6k

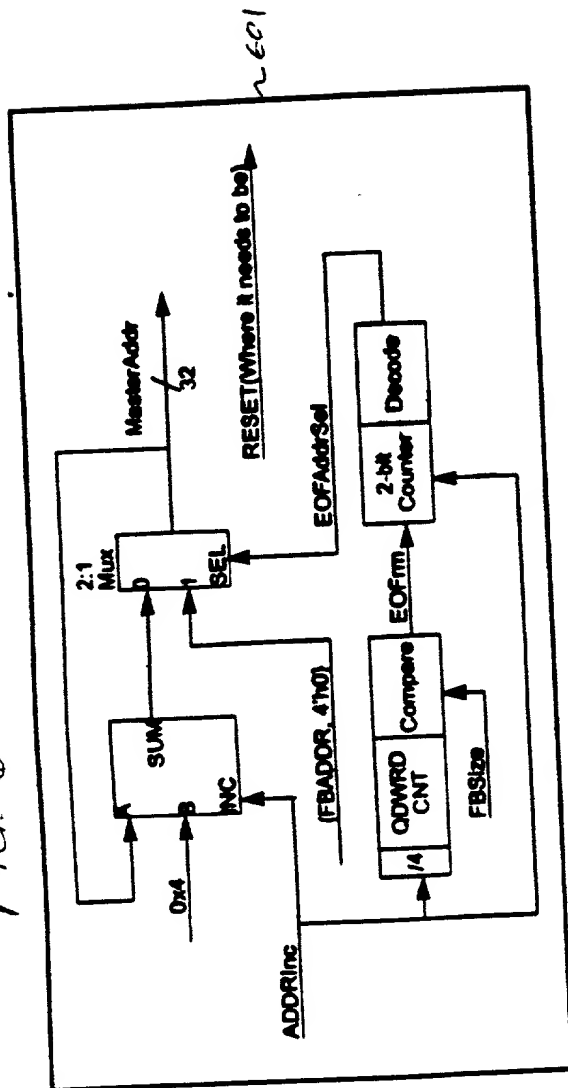


FIG 6M

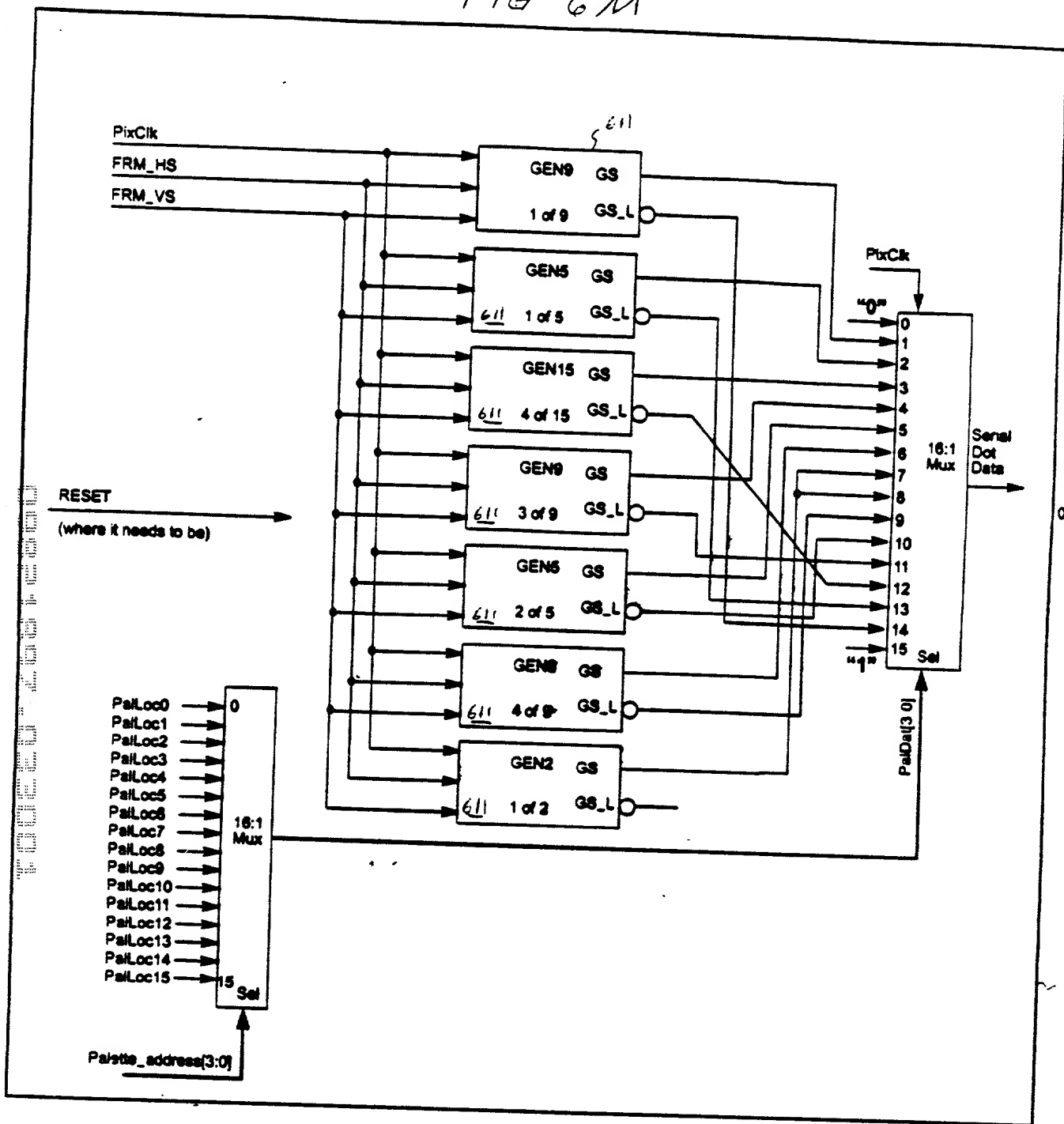
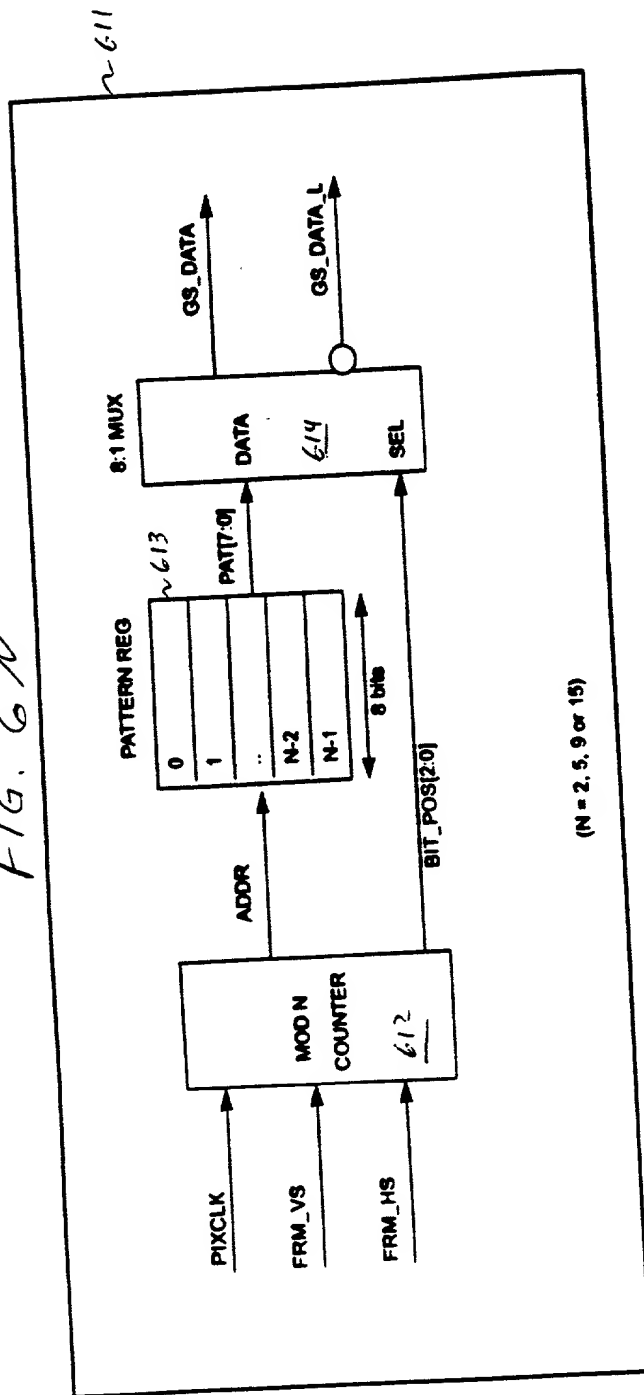


FIG. 6N



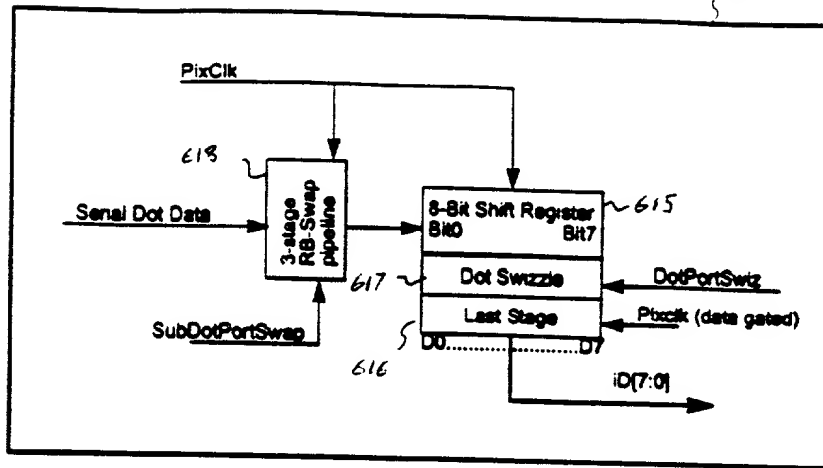
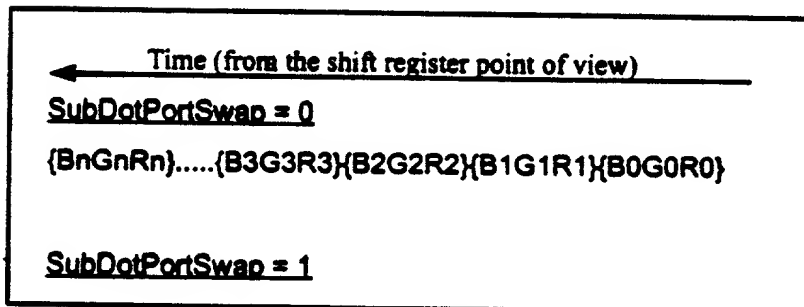


FIG 60

FIG. 6P



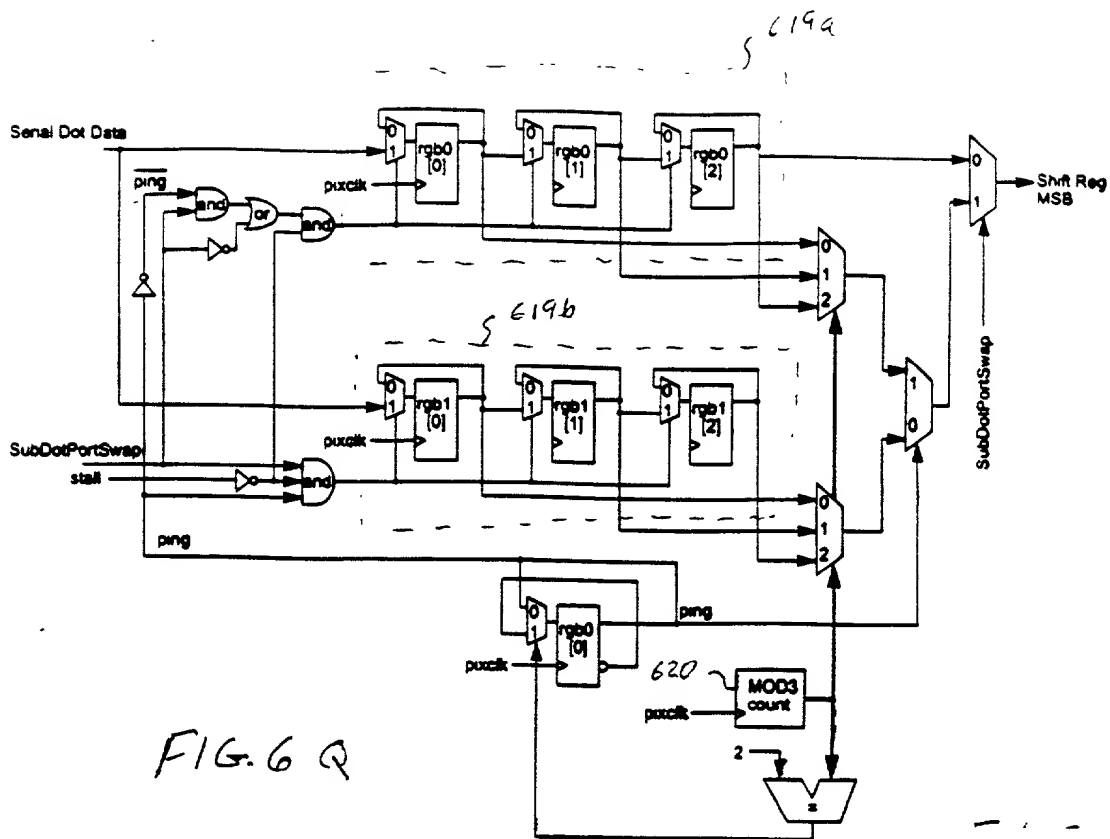


FIG 6.R

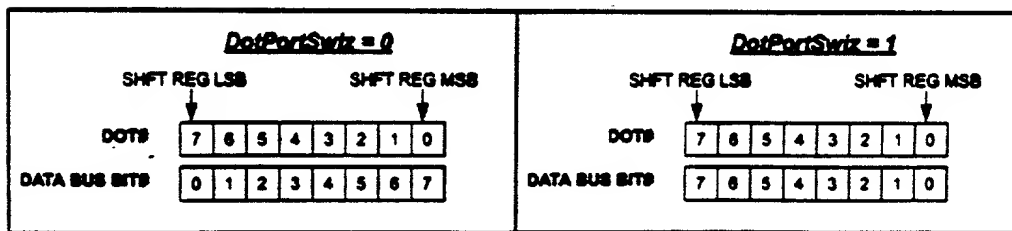


FIG. 6 S

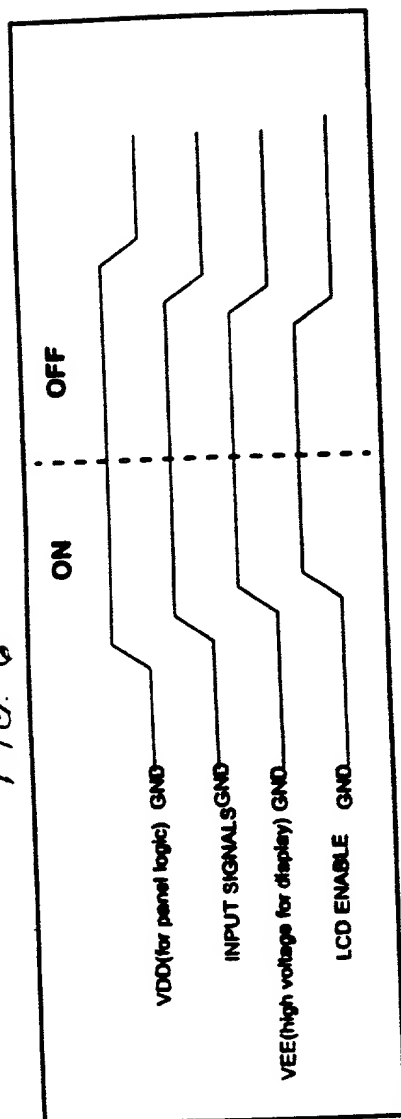


FIG. 7A

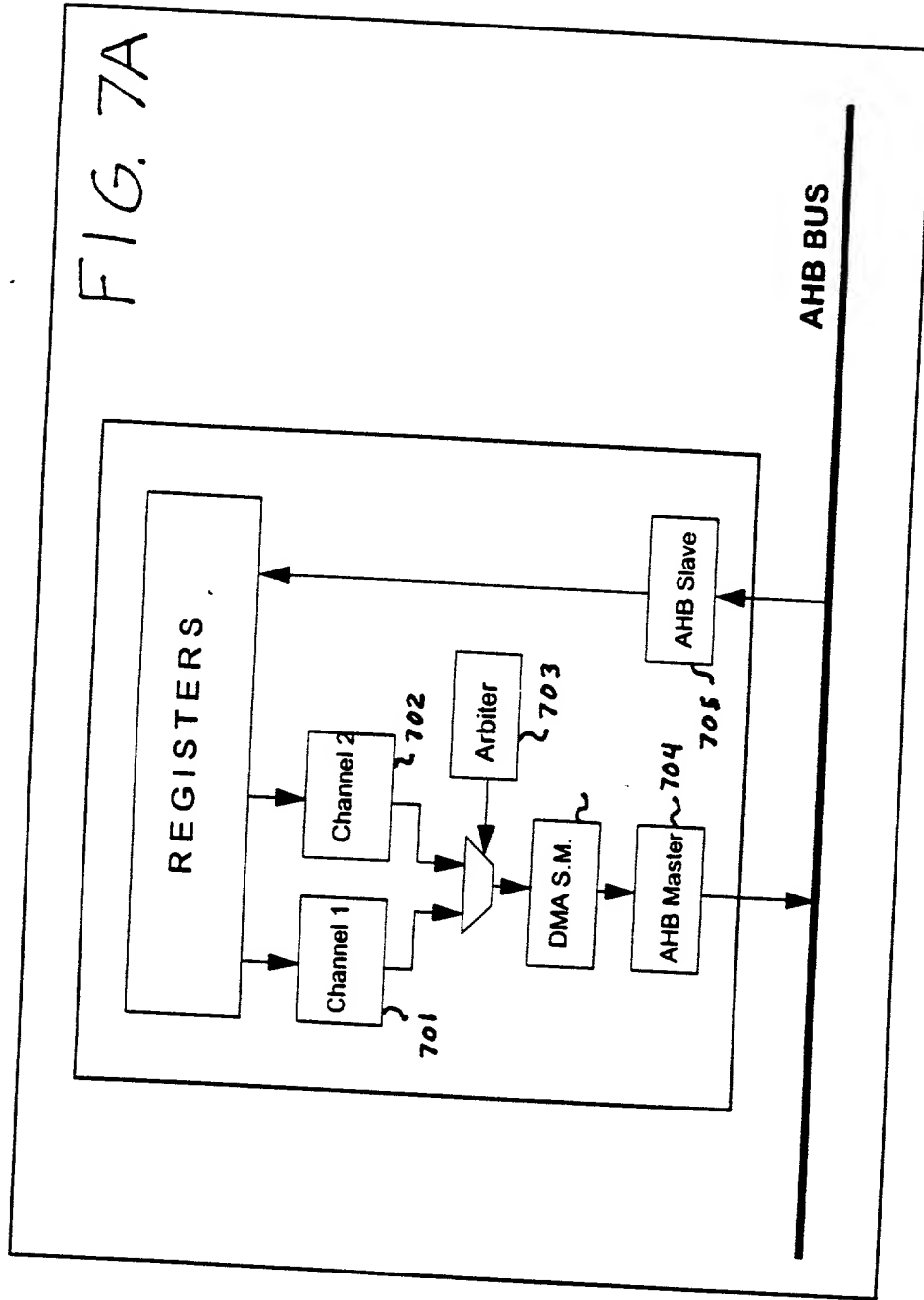


FIG. 7B

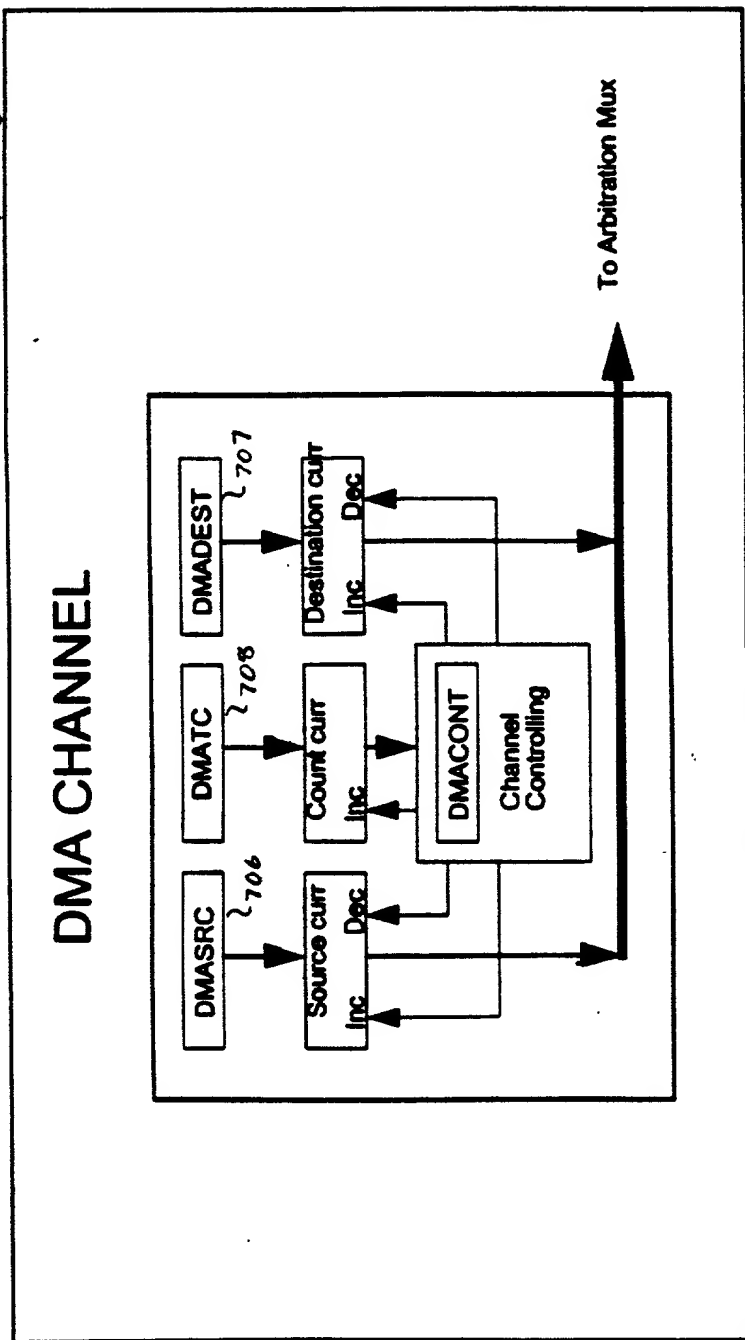
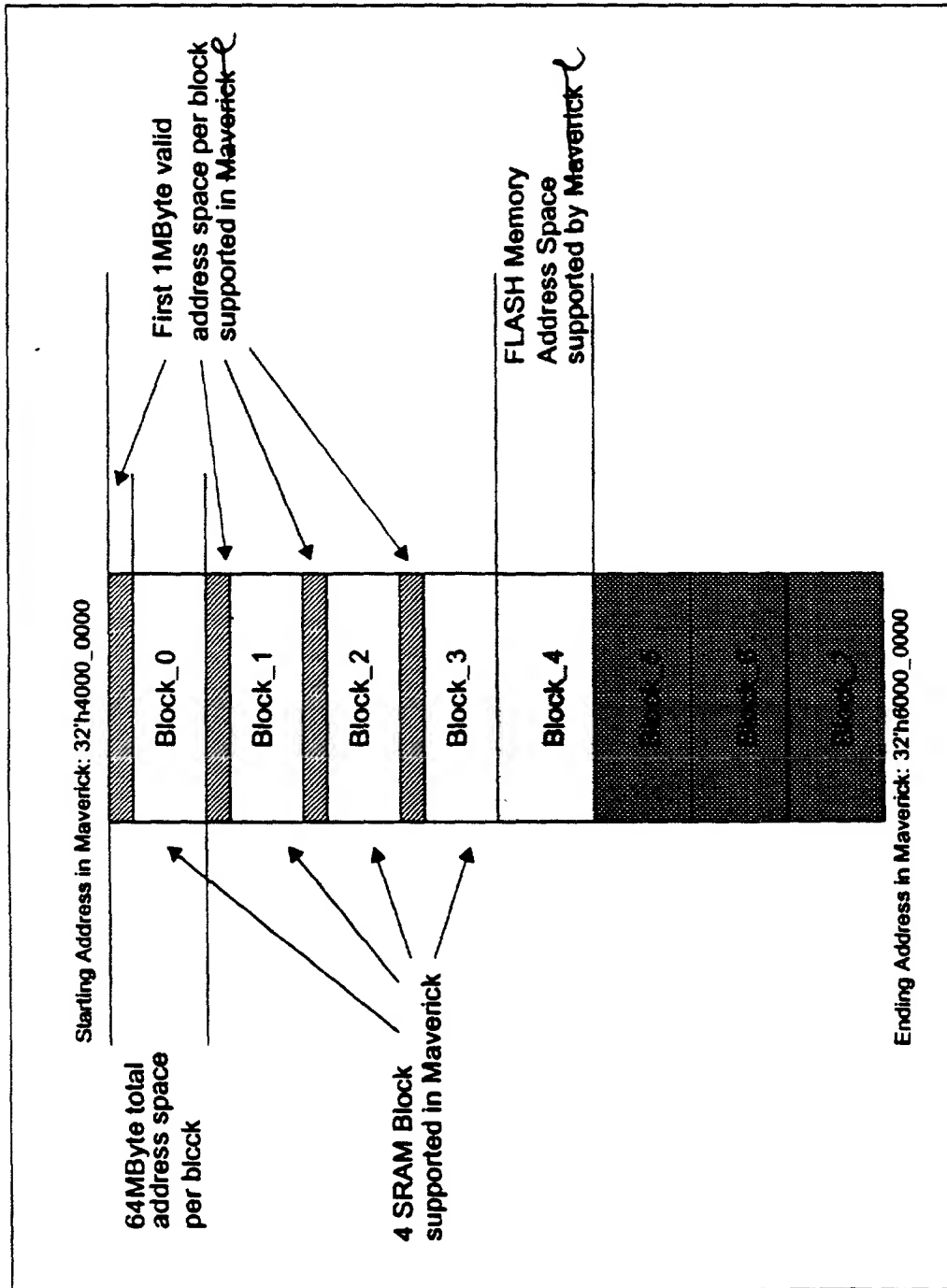
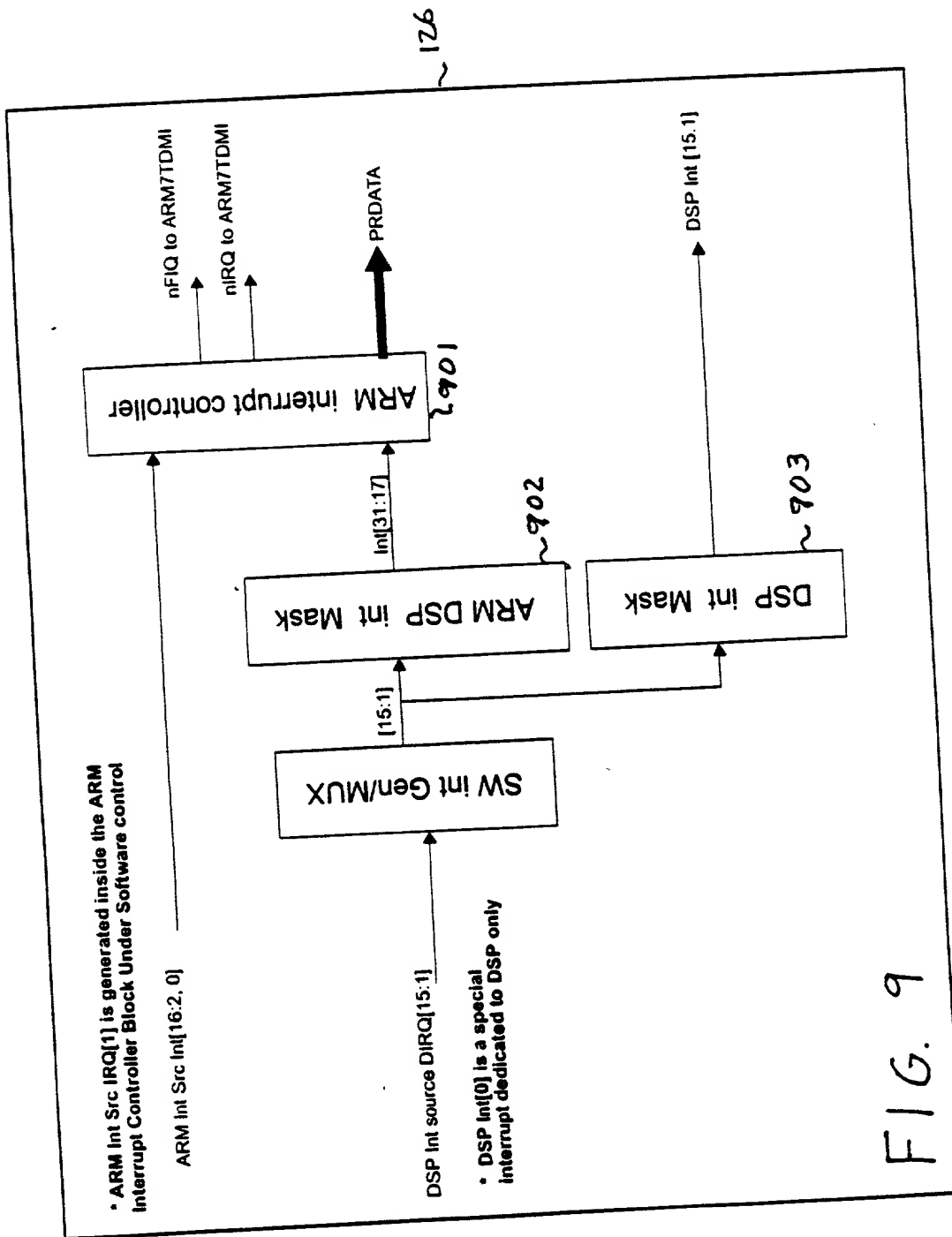


FIG. 8





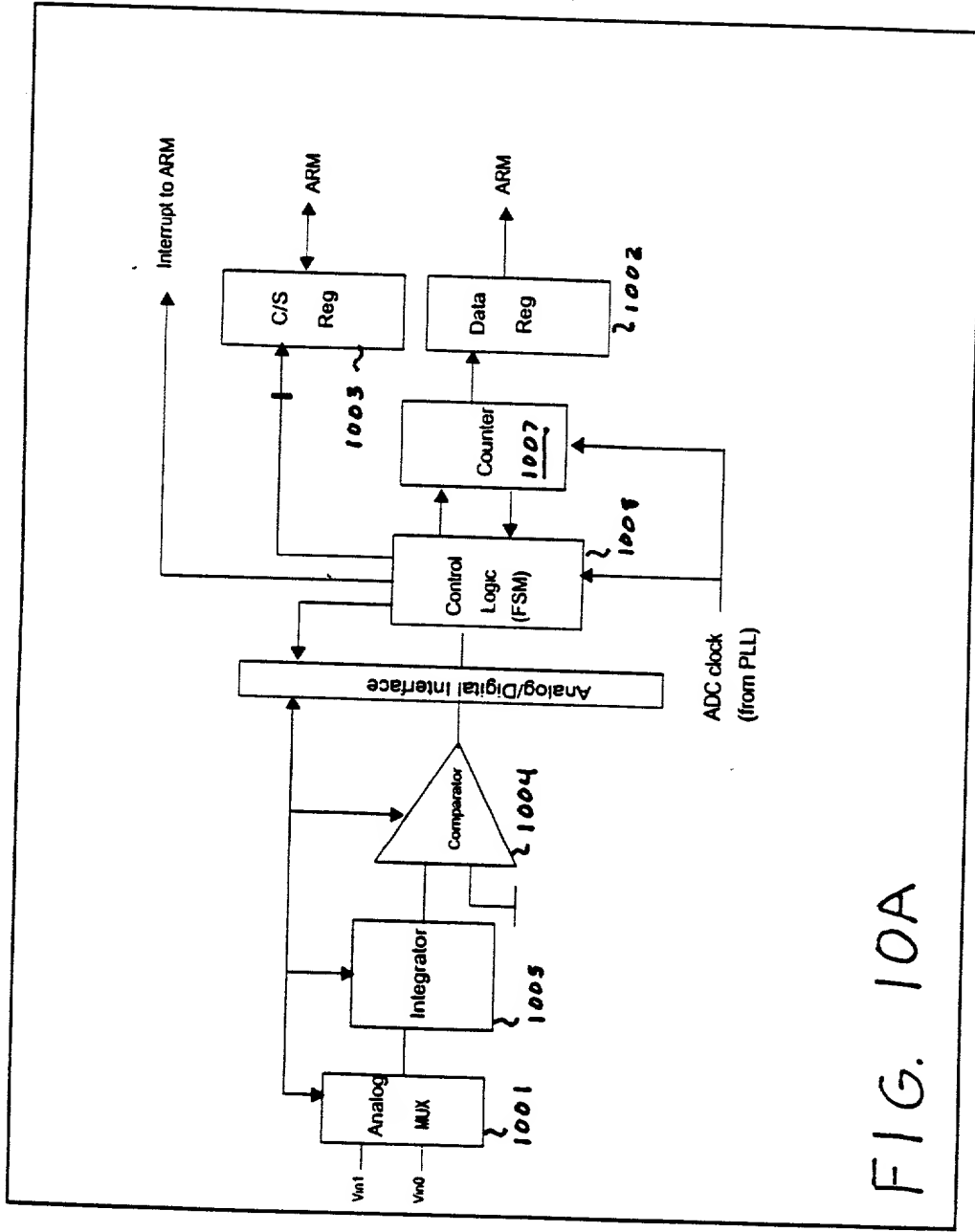


FIG. 10A

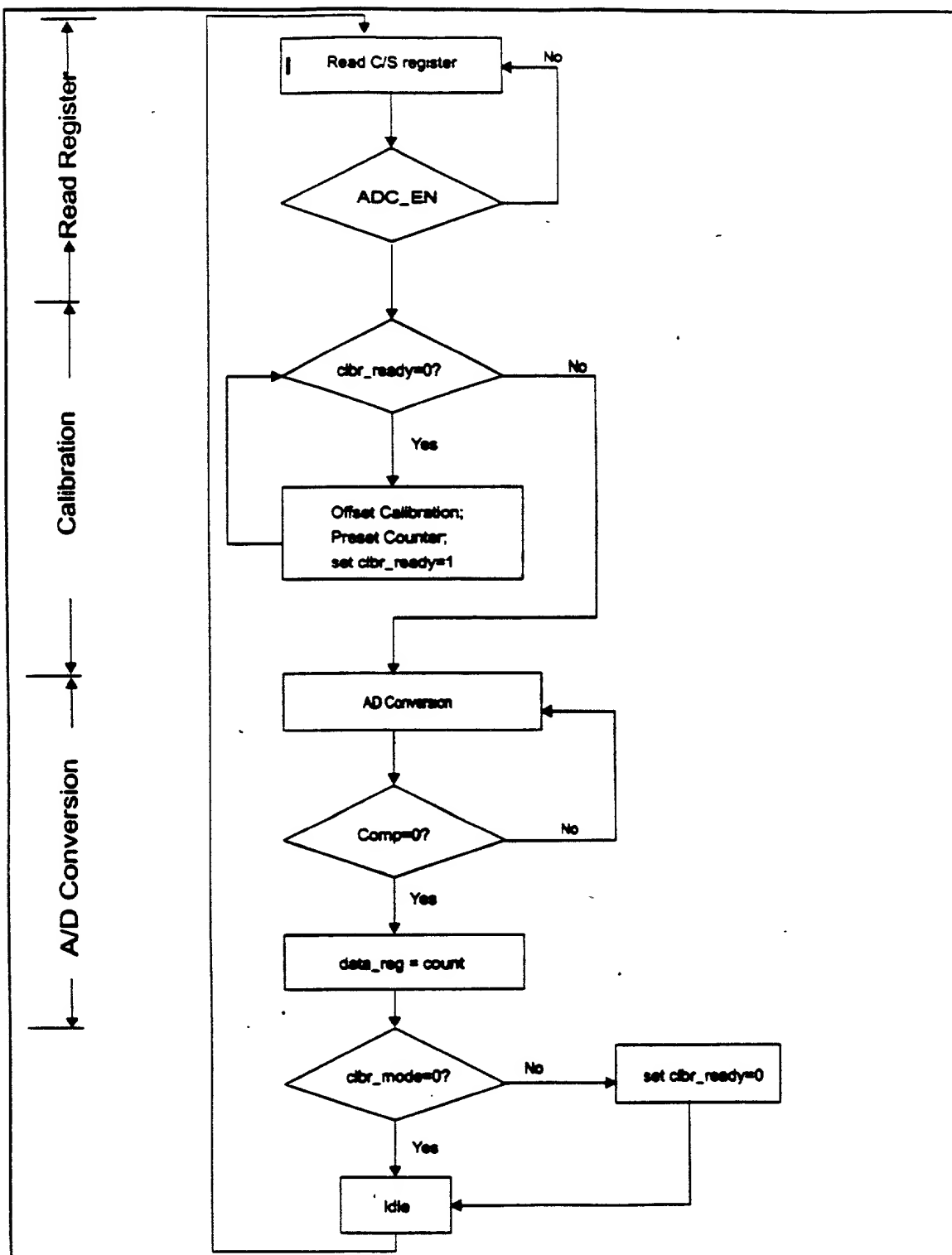
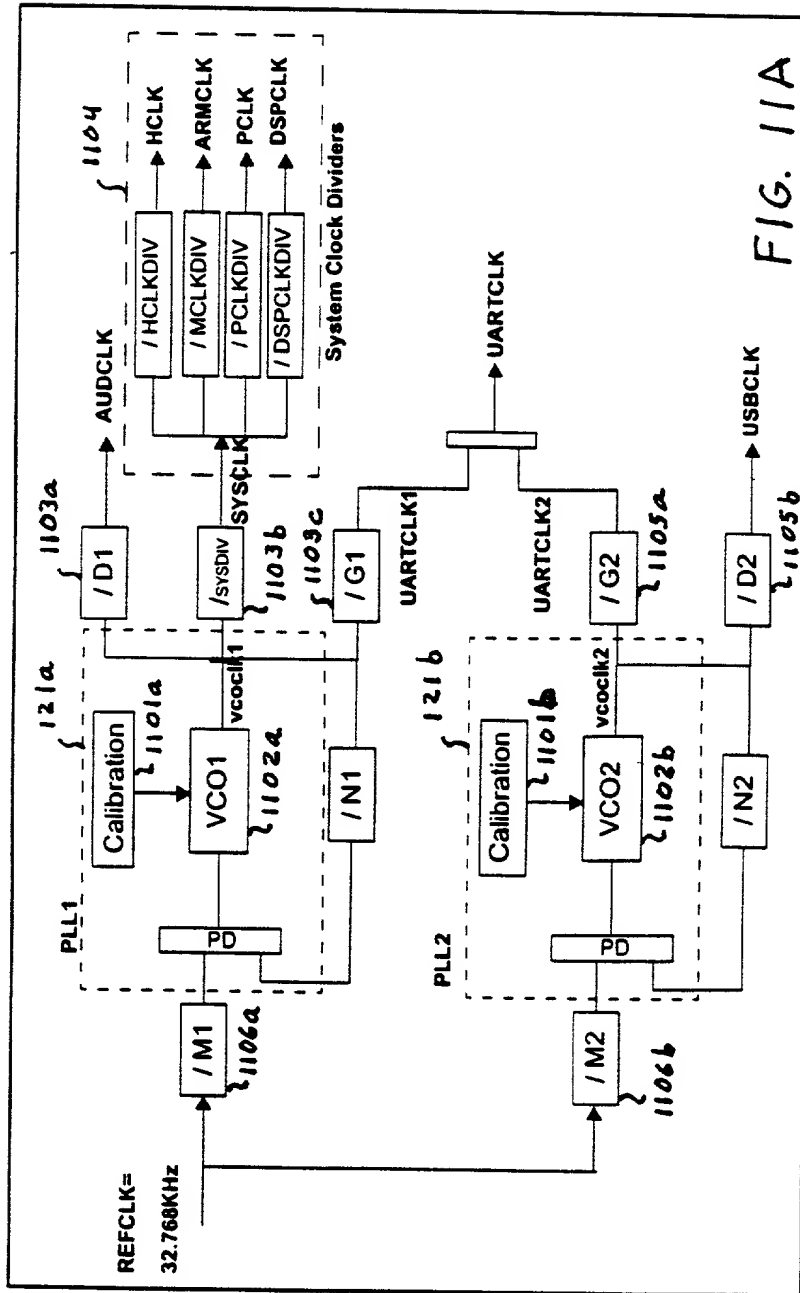


FIG. 10B



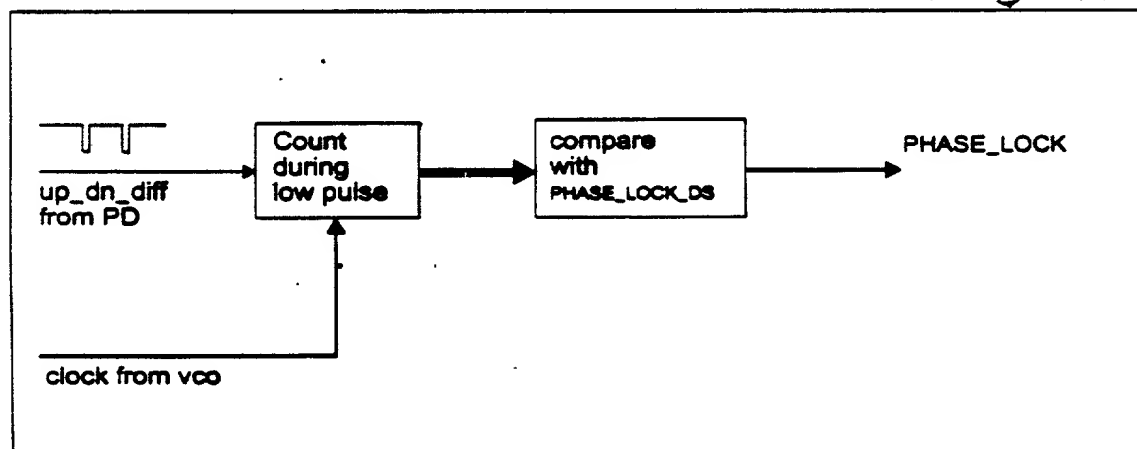
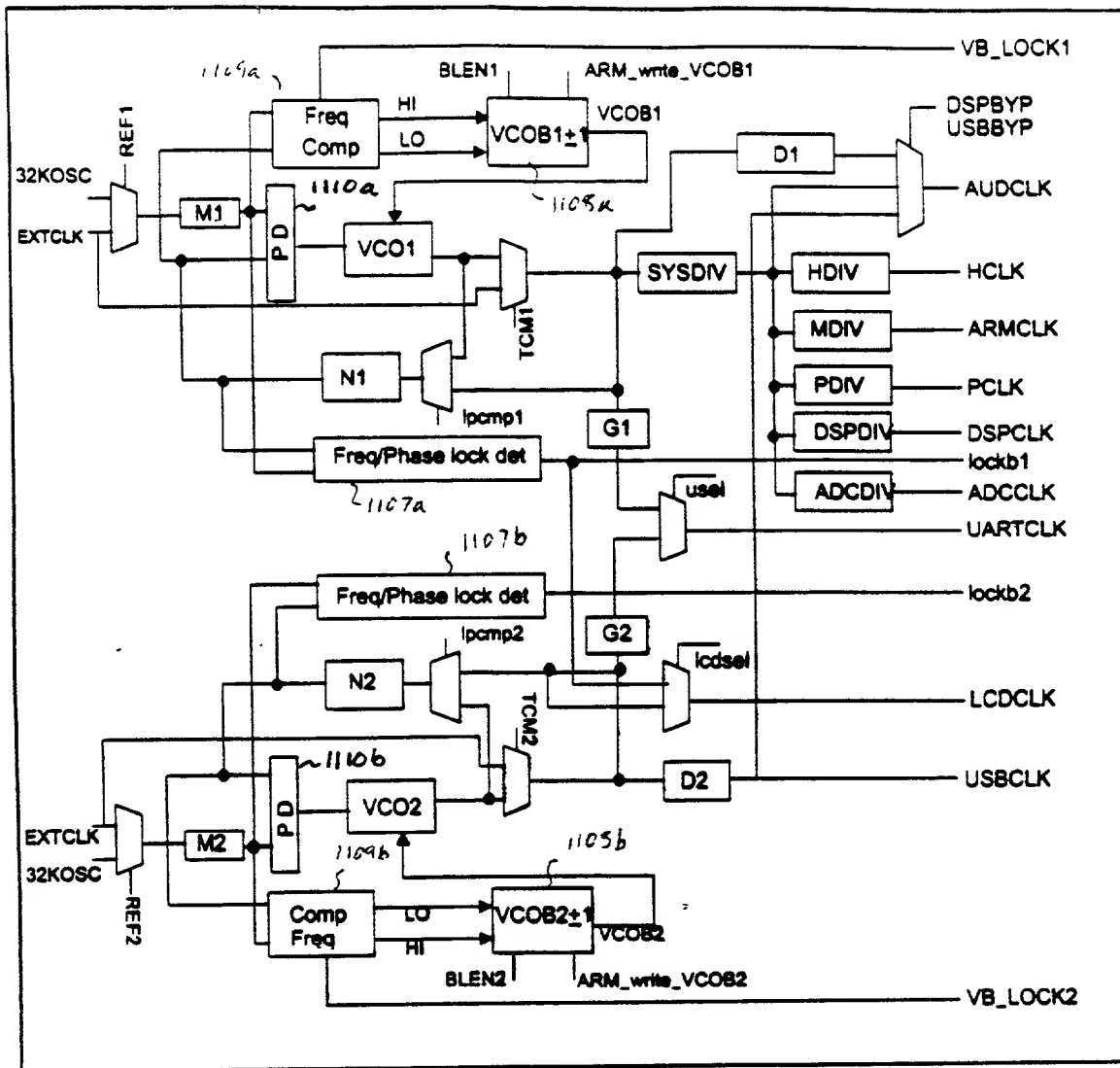
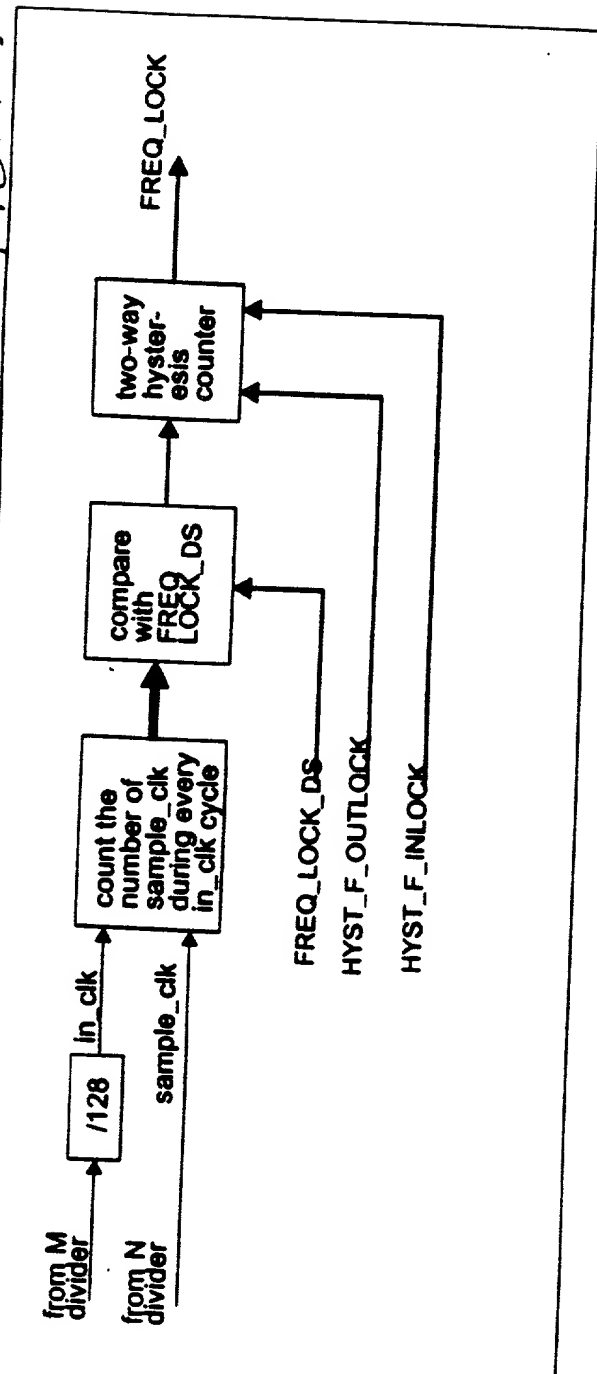


FIG. 11D



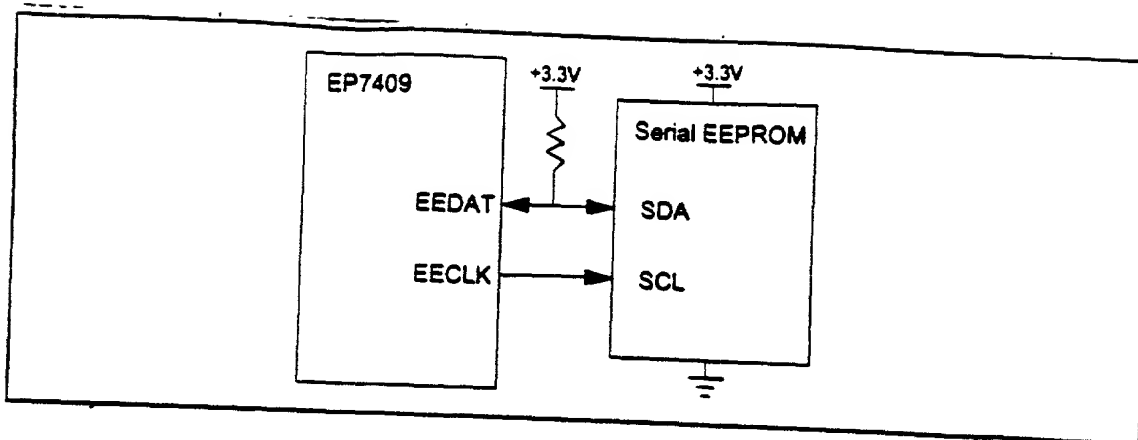


Fig. 12 A

Fig 12 B

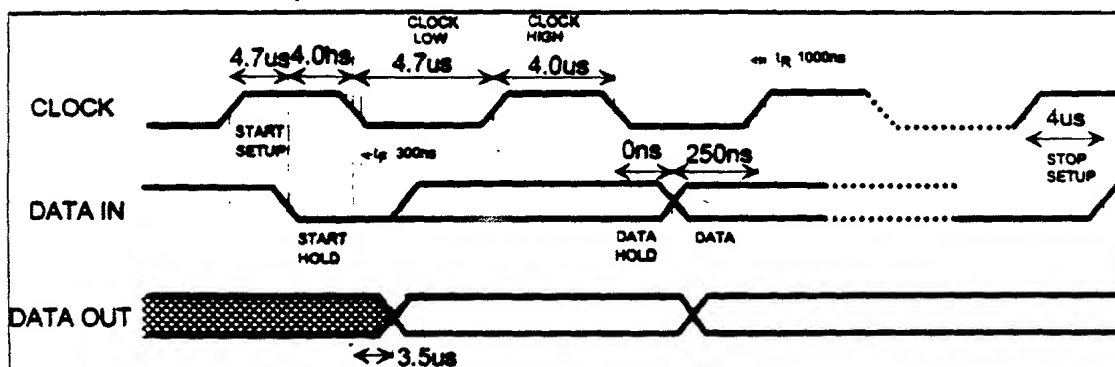
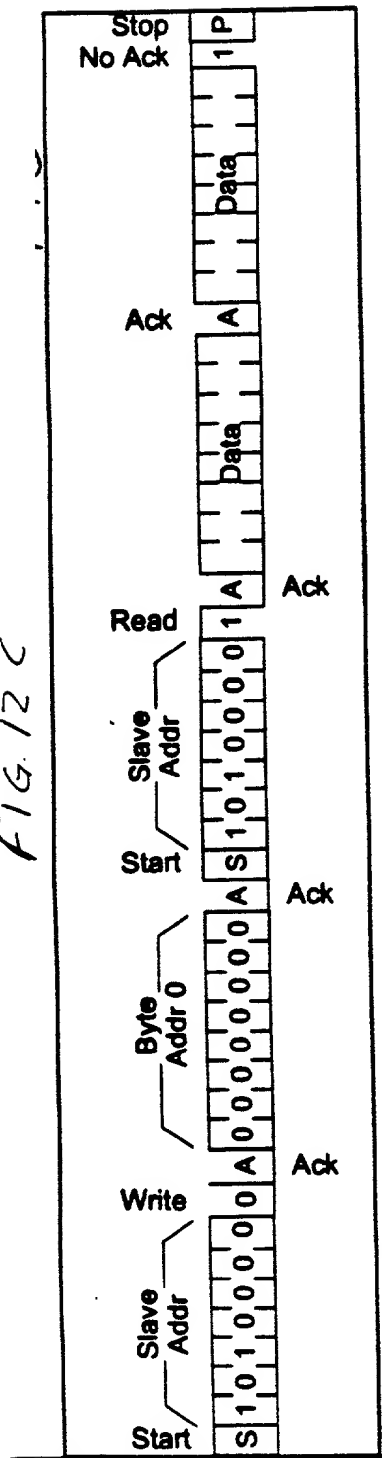


FIG. 12C



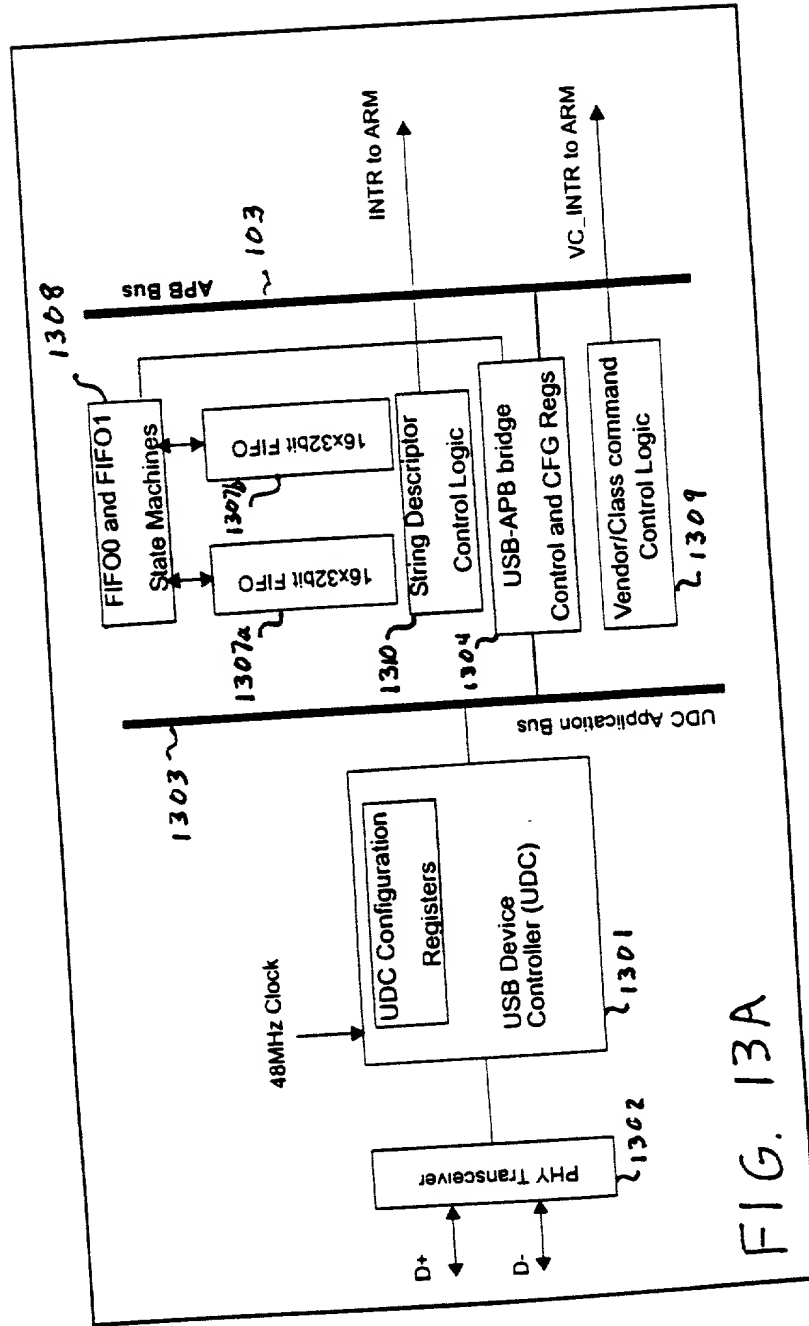


FIG. 13A

FIG. 13B

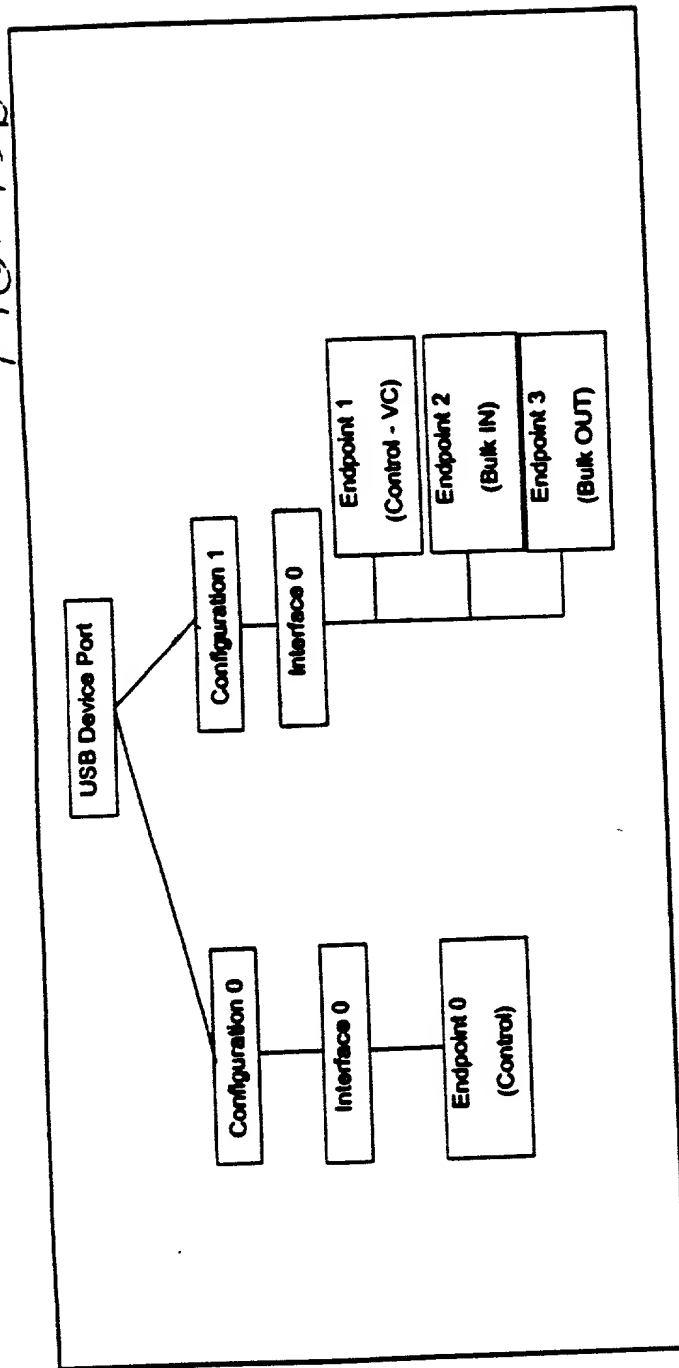


FIG. 14

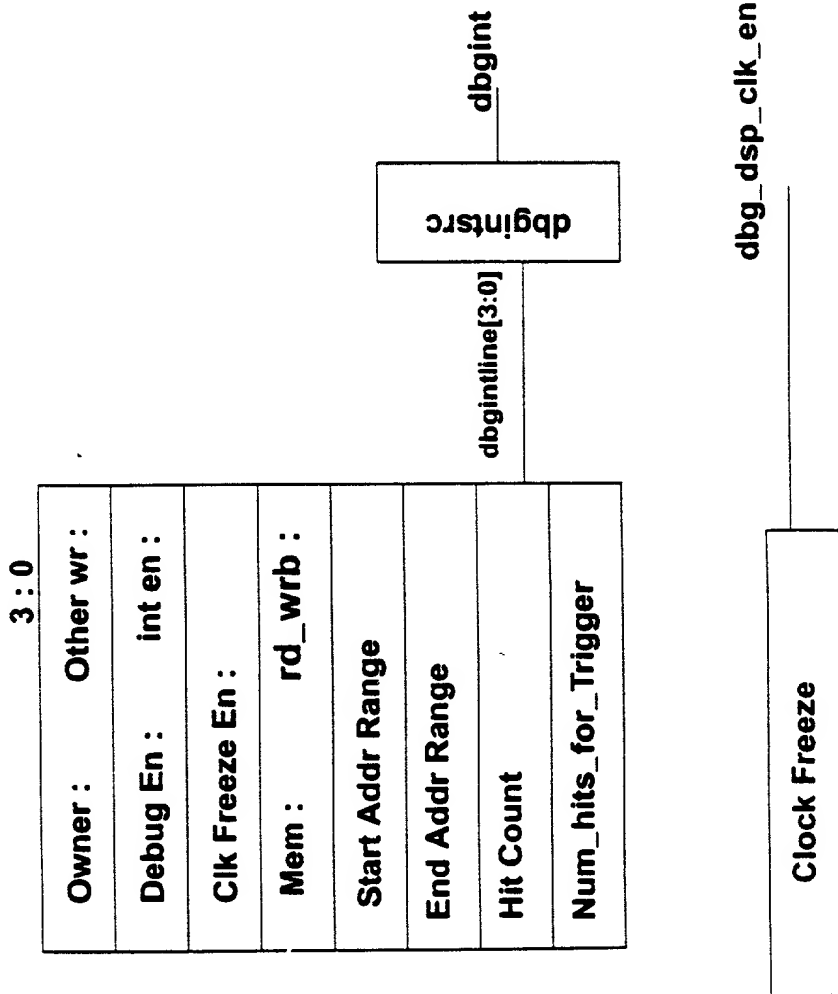
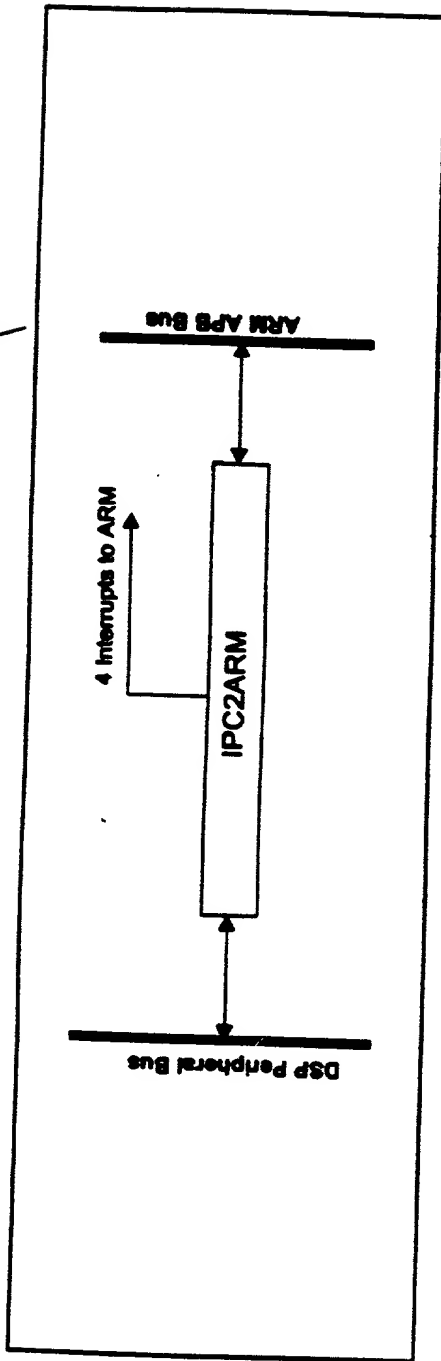


FIG. 15



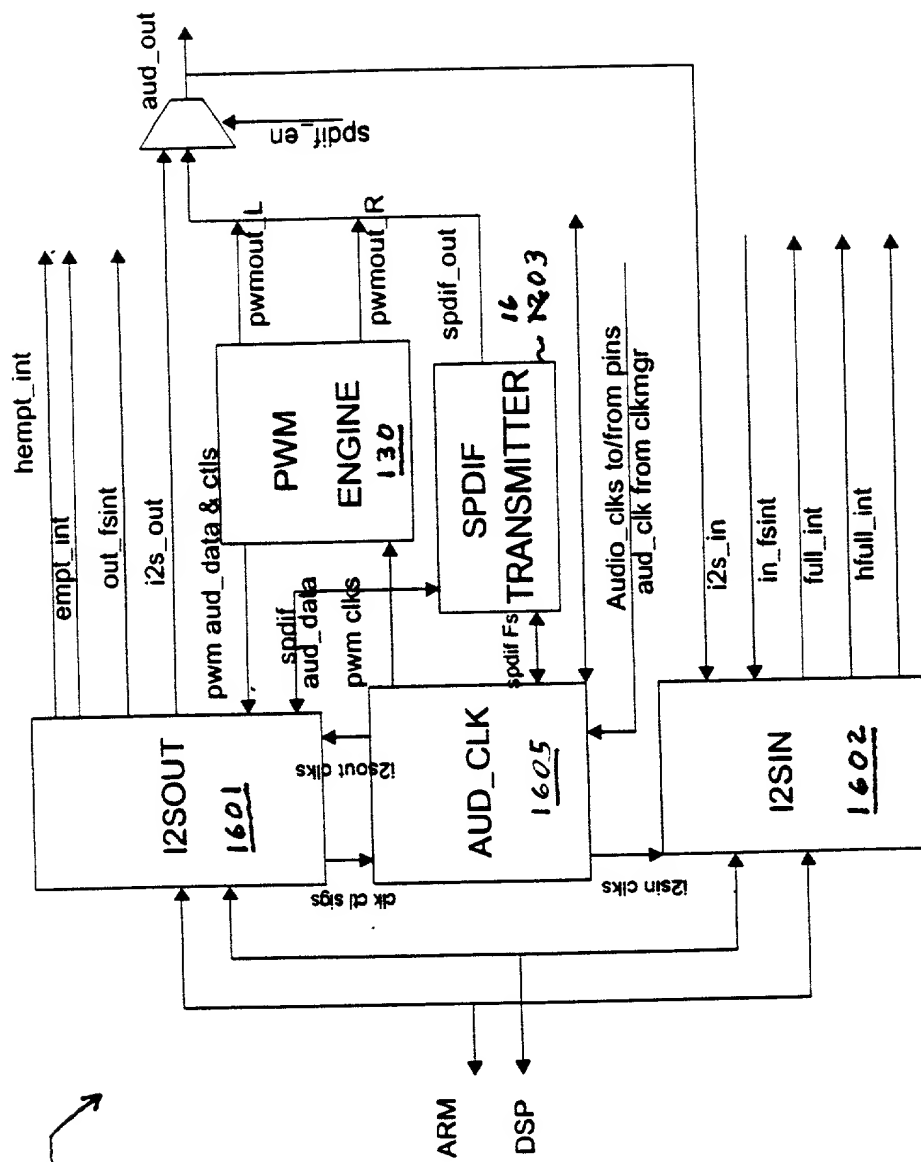


FIG. 16A

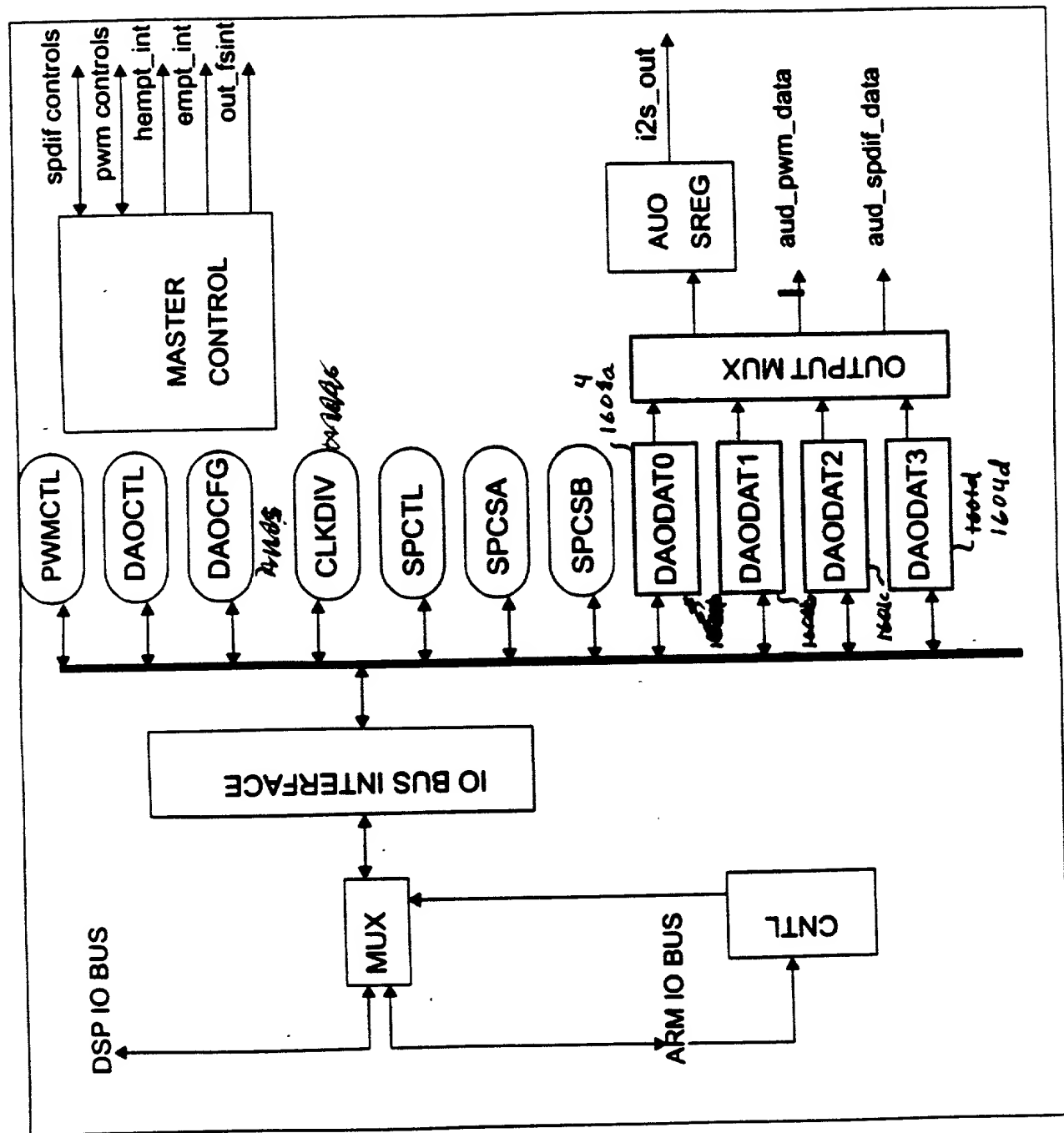


FIG. 16B

FIG. 16C

The following figure shows a typical output waveform.

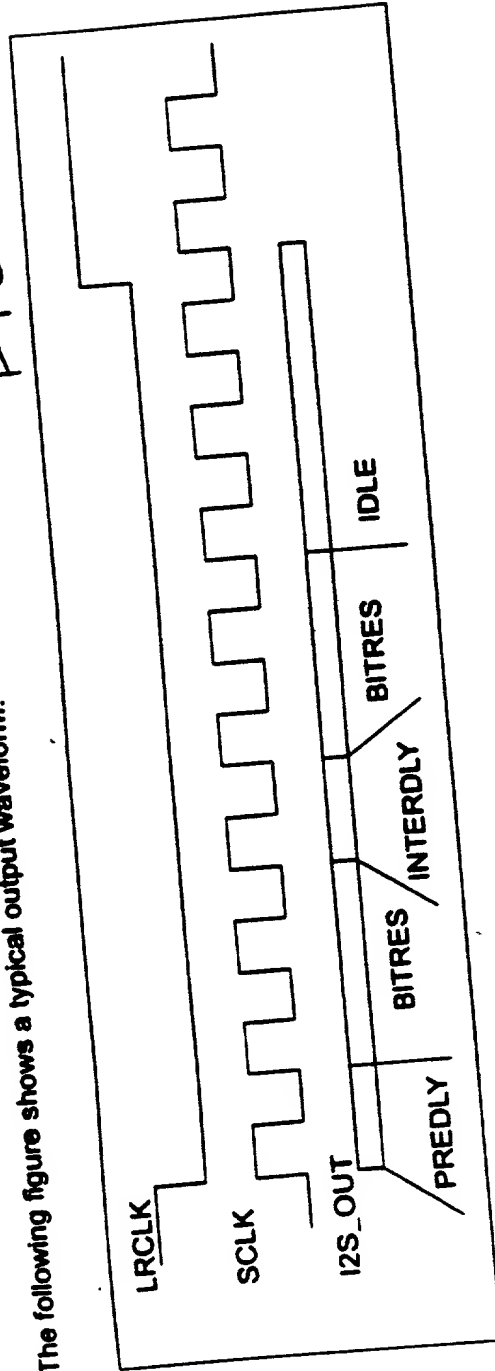


FIG. 16D

1602

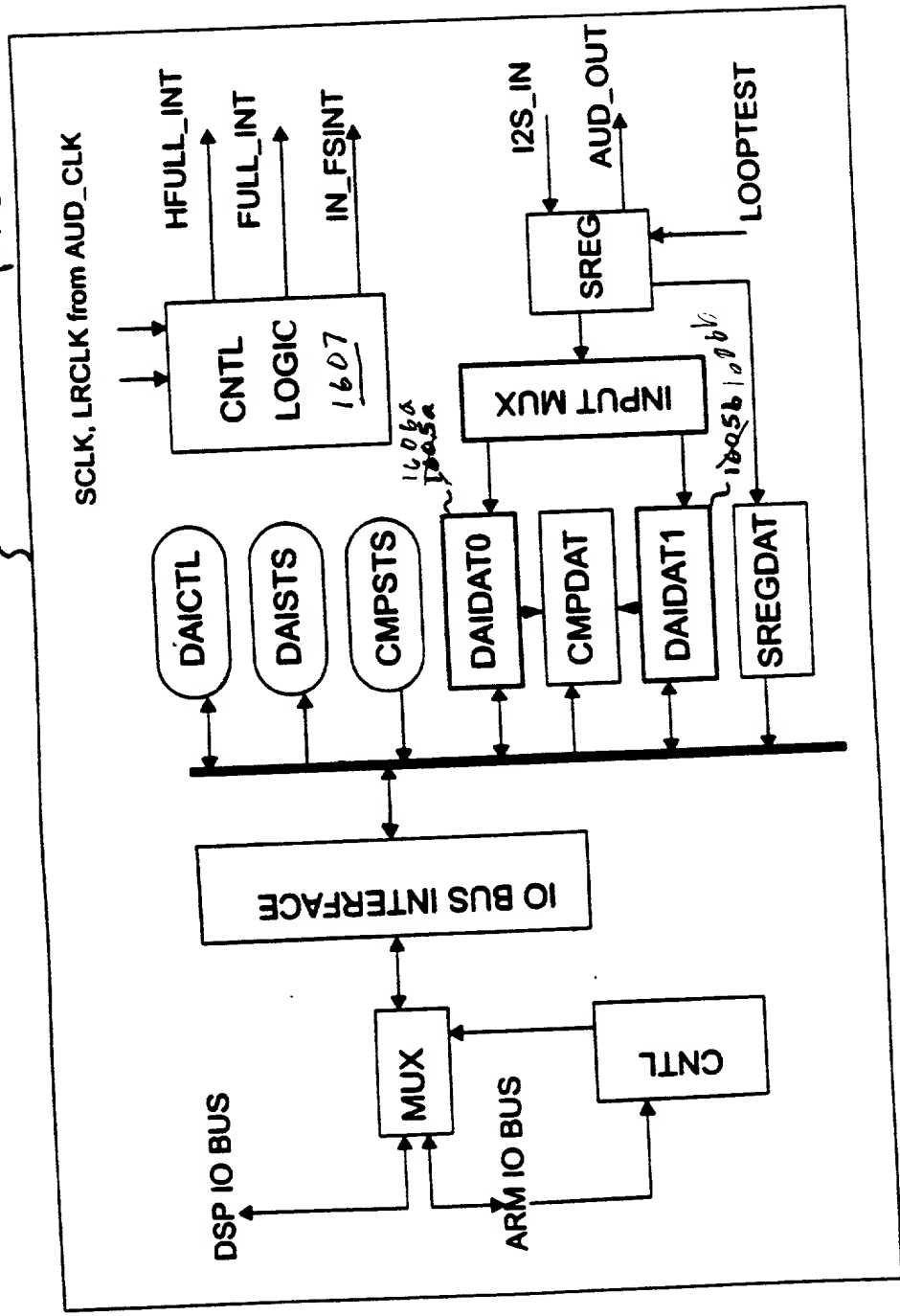


FIG. 17

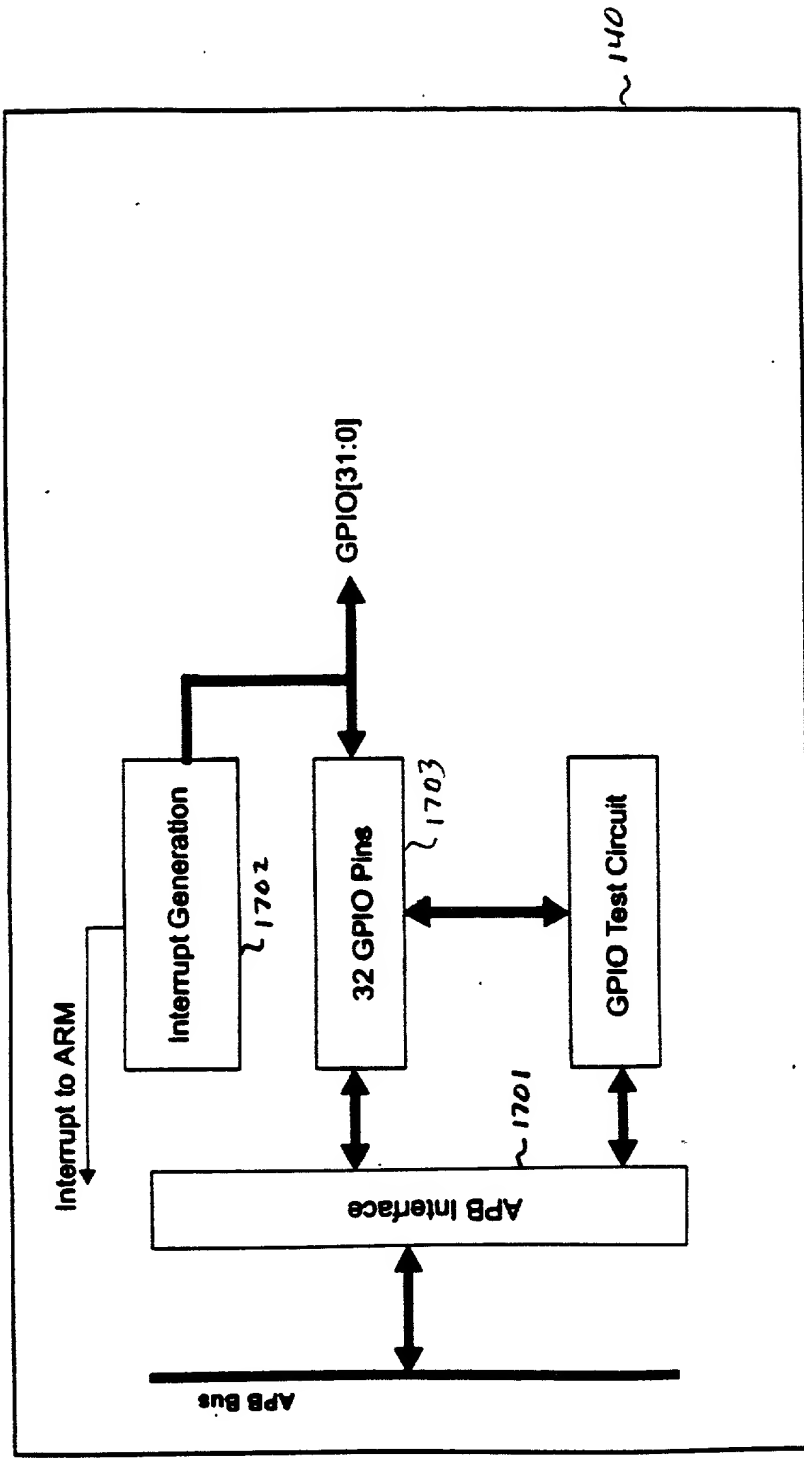


FIG. 18

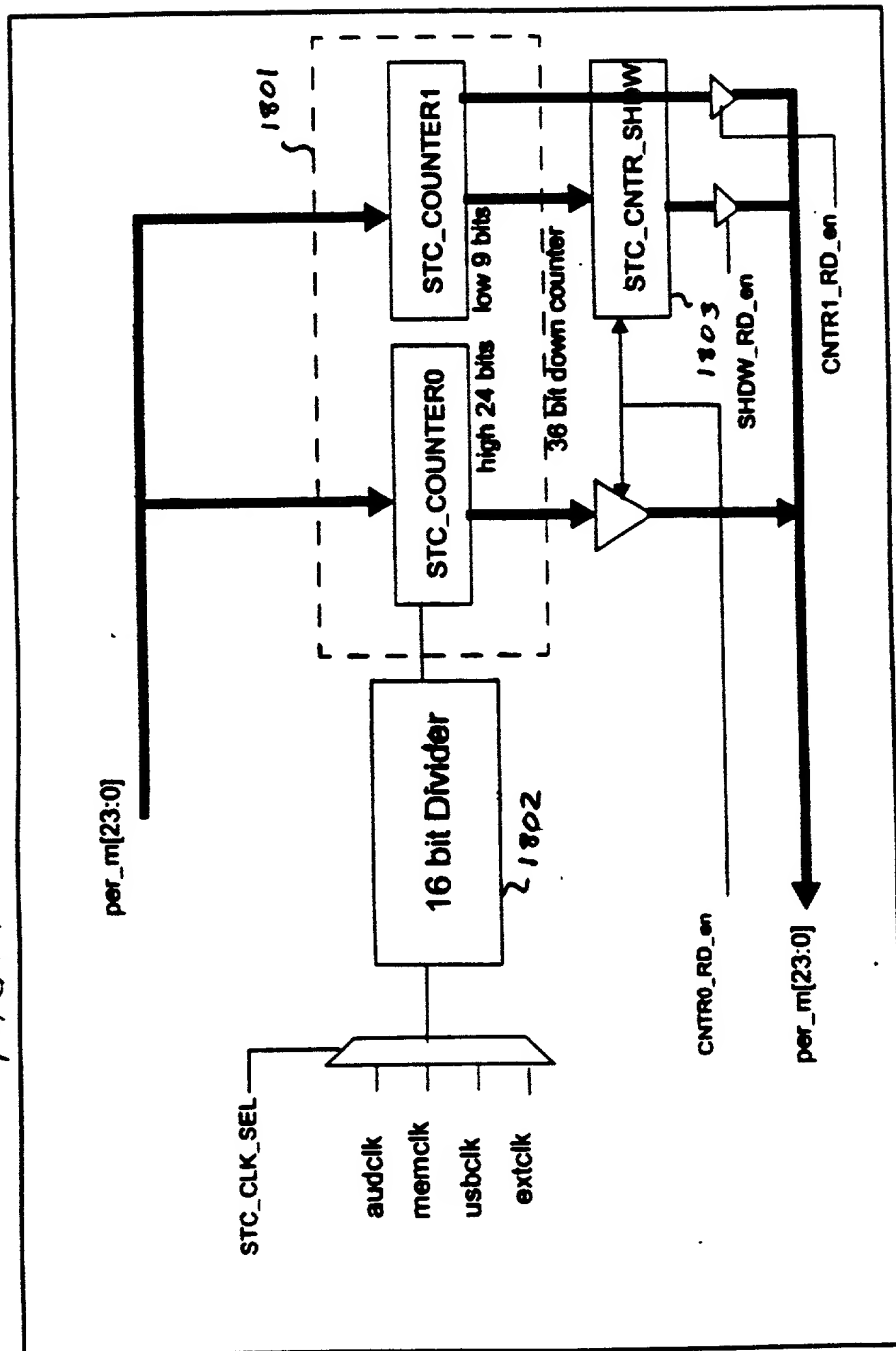


FIG. 19A

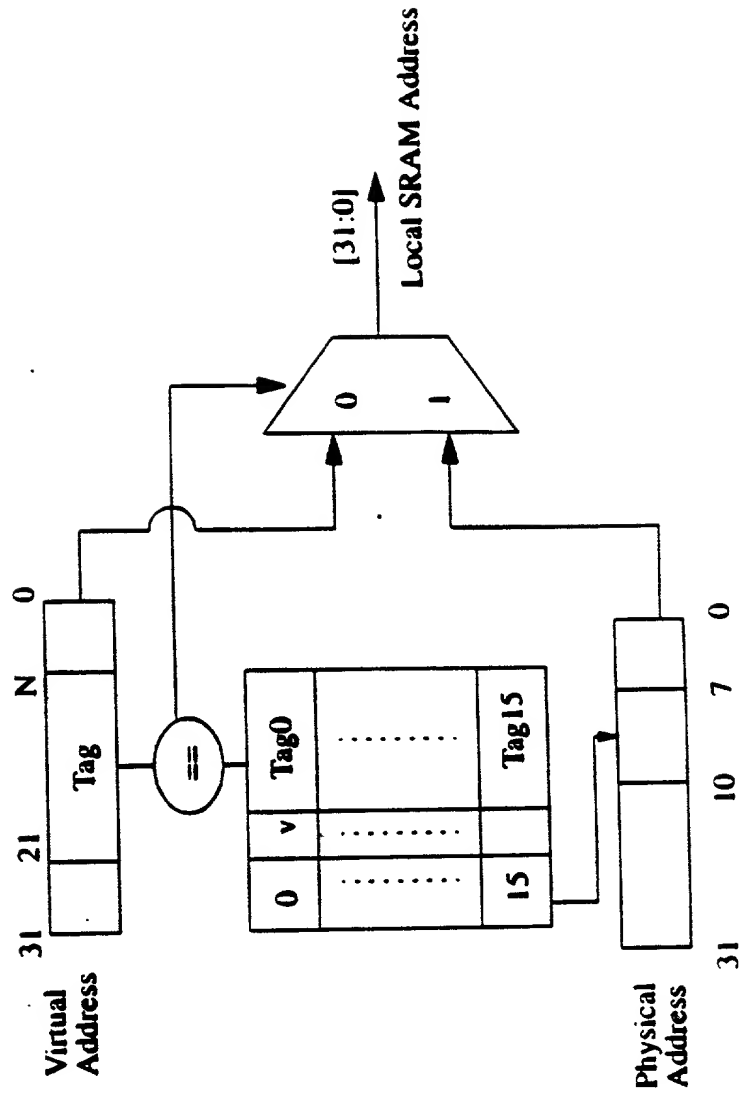
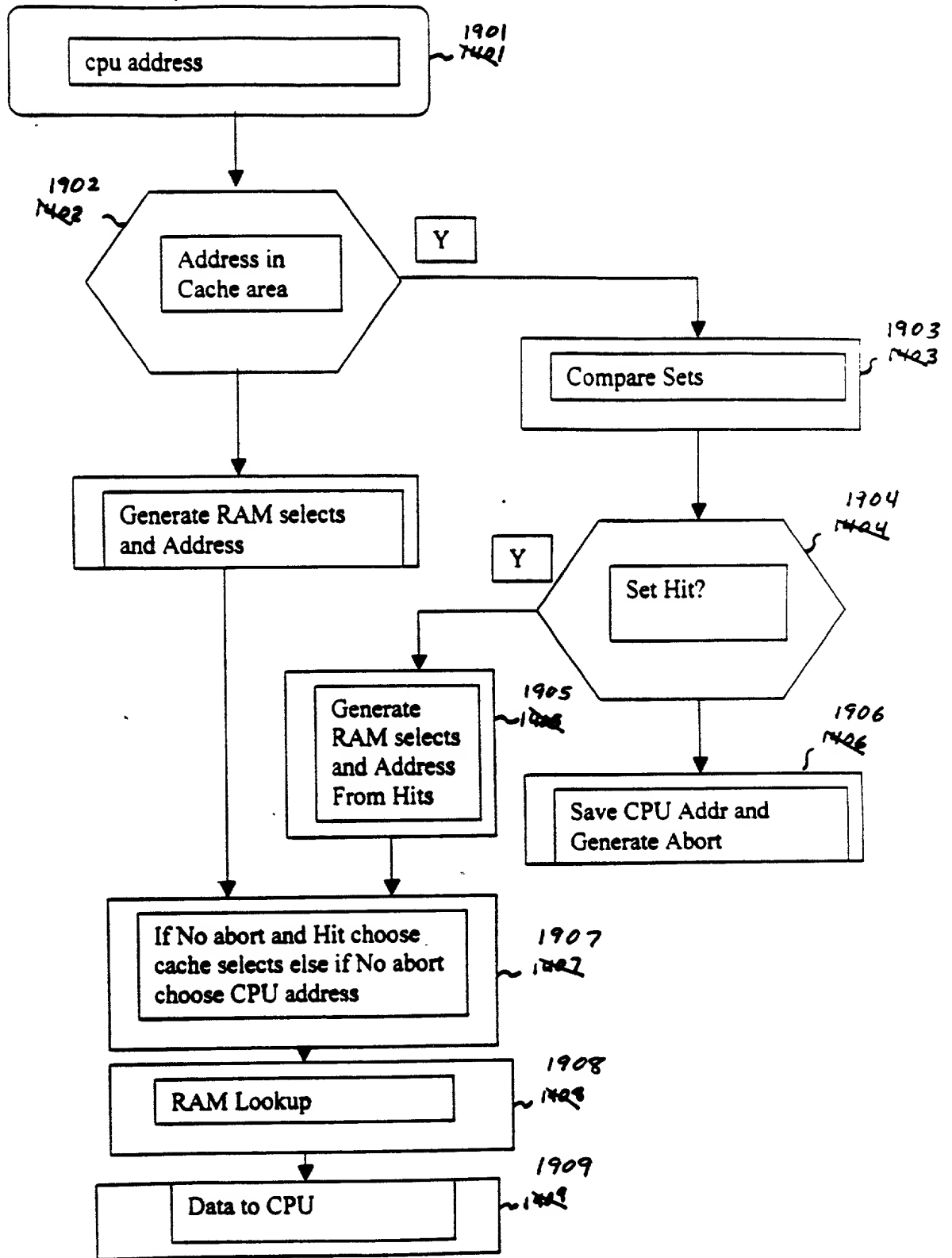


FIG 19B



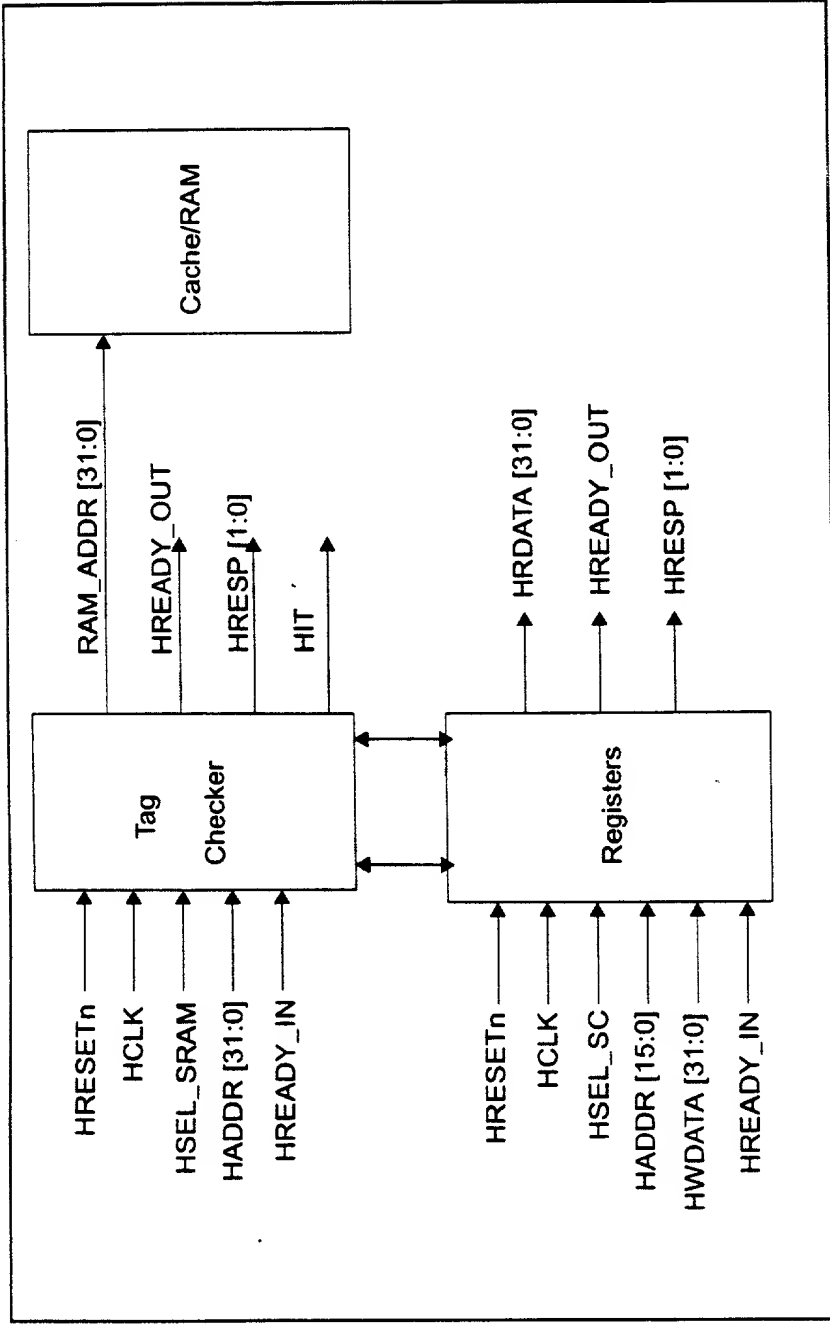


FIG. 19C

Fig. 20A

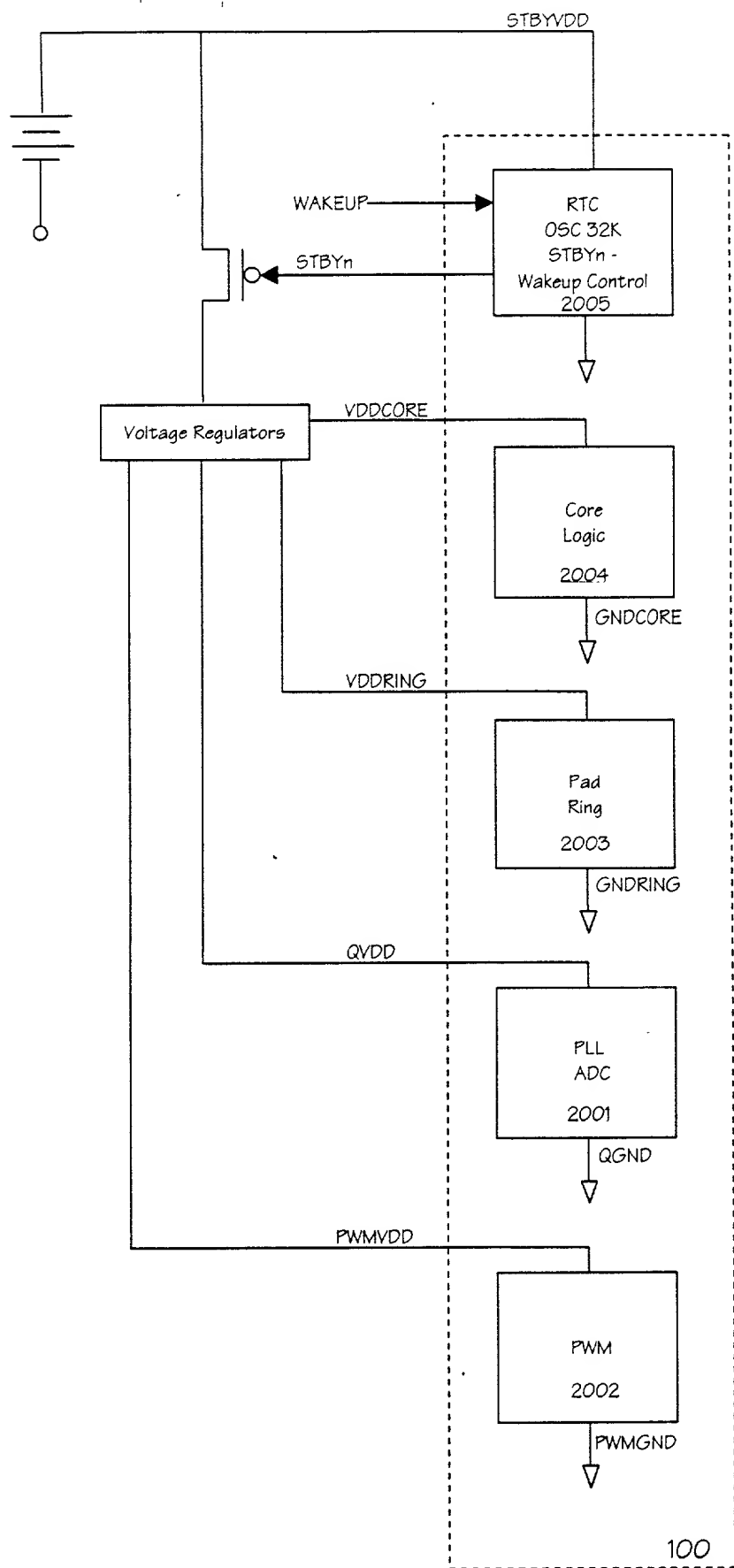


FIG. 20B

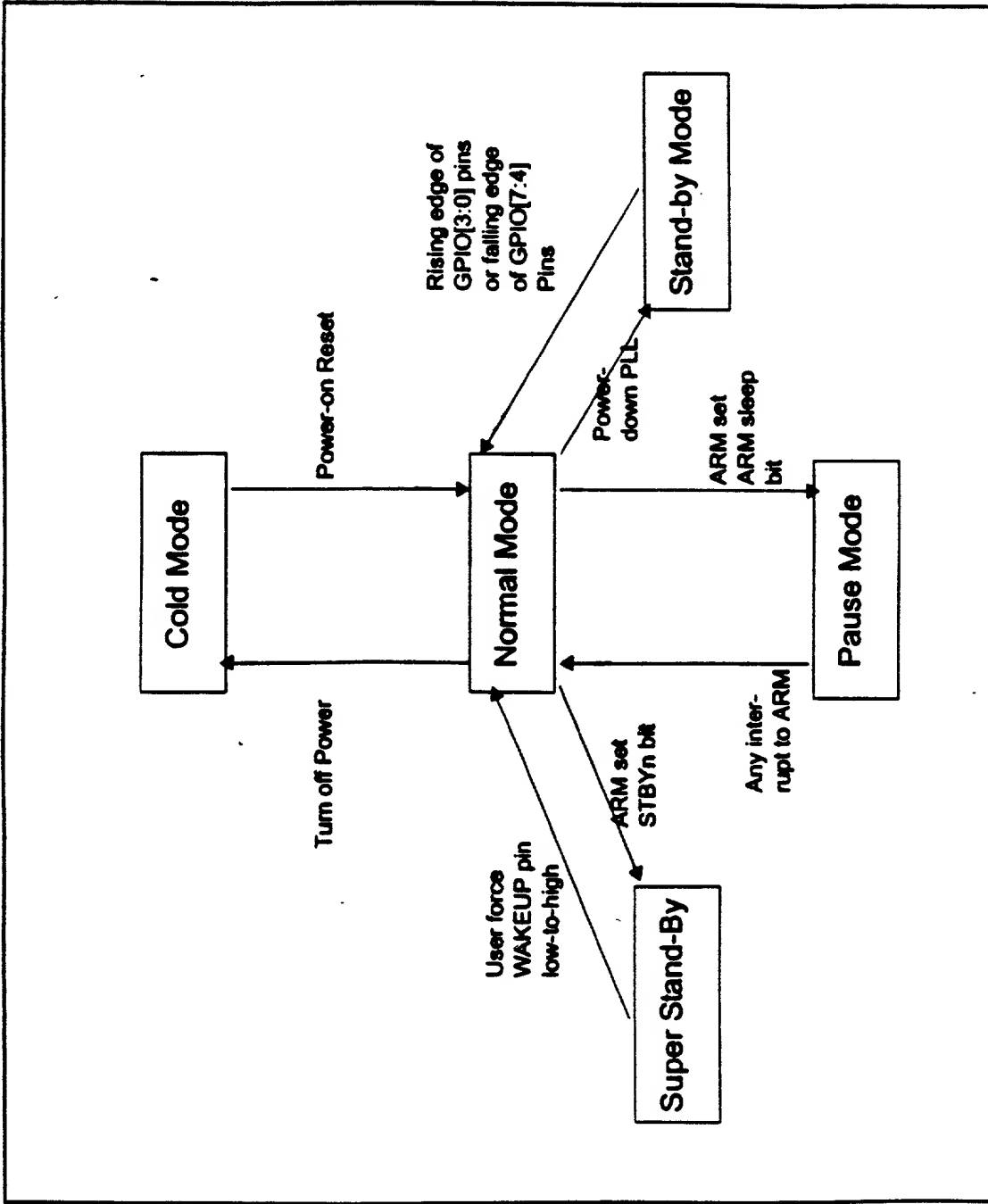


Fig. 20 C 5
Sheet 1 of 3

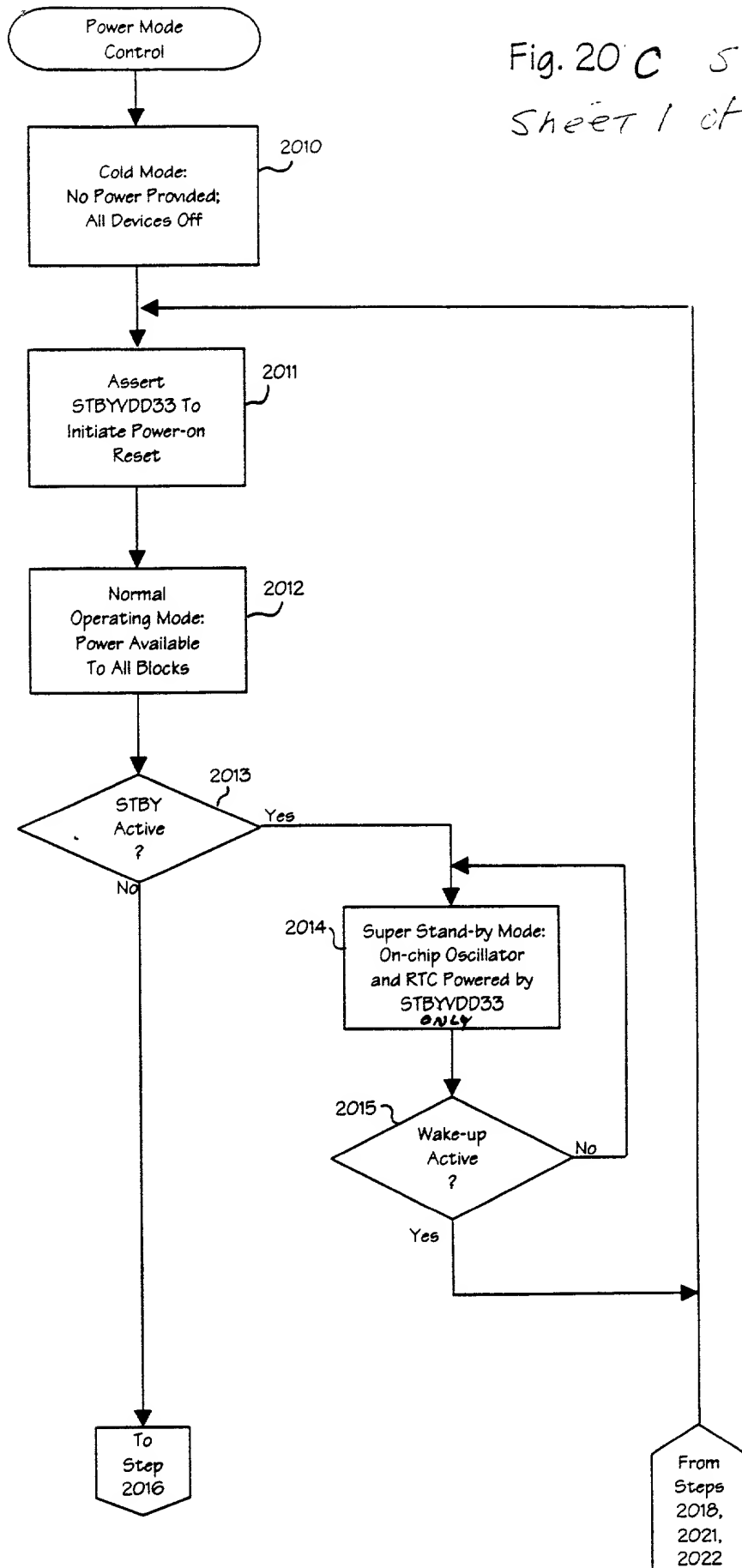
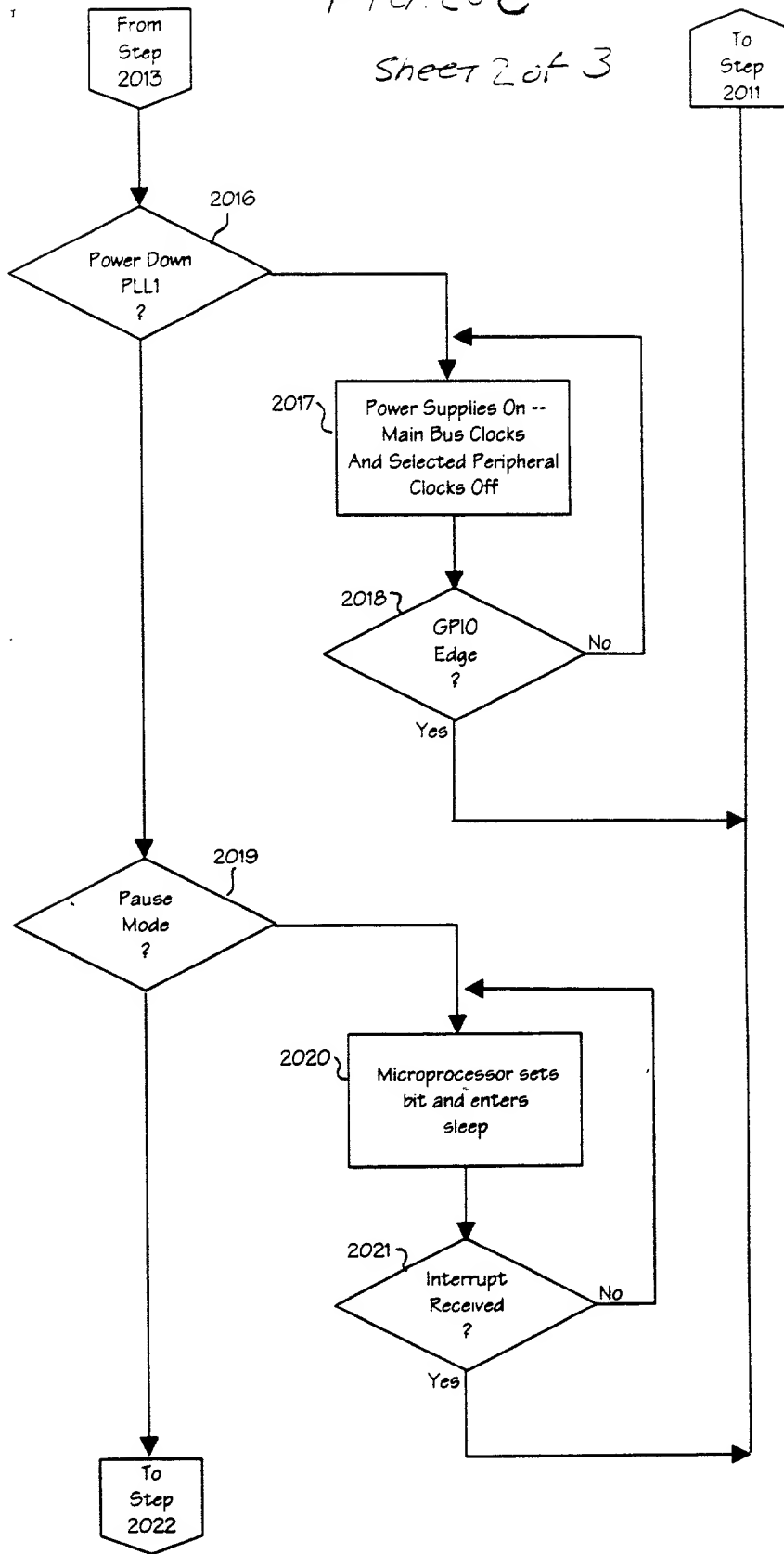


FIG. 20C

Sheet 2 of 3



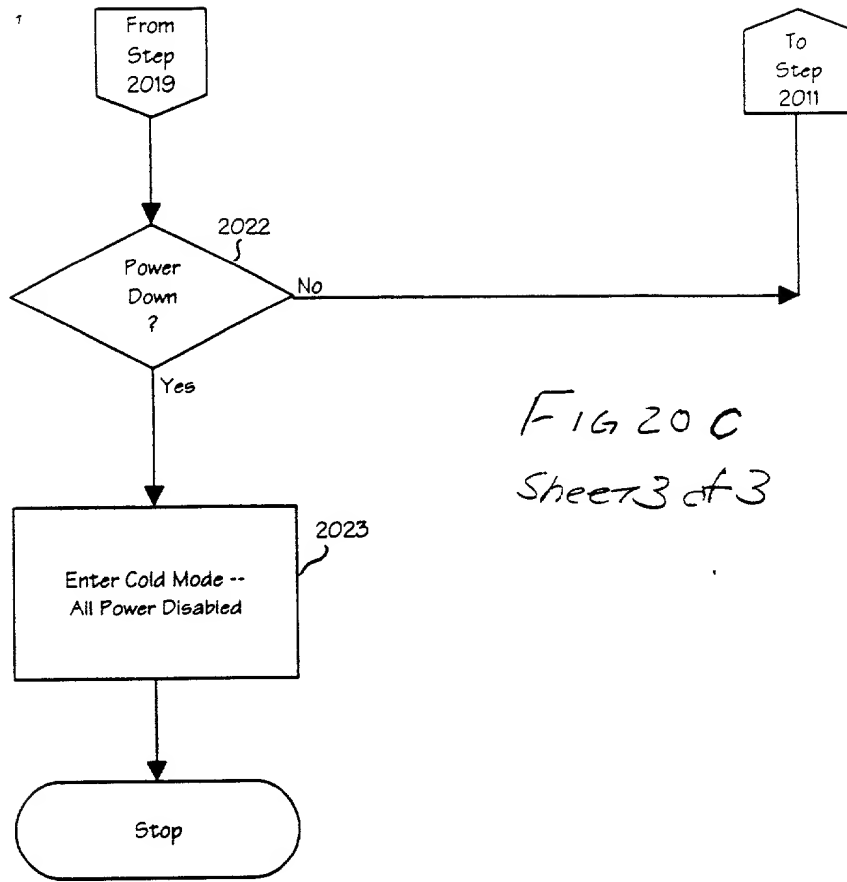
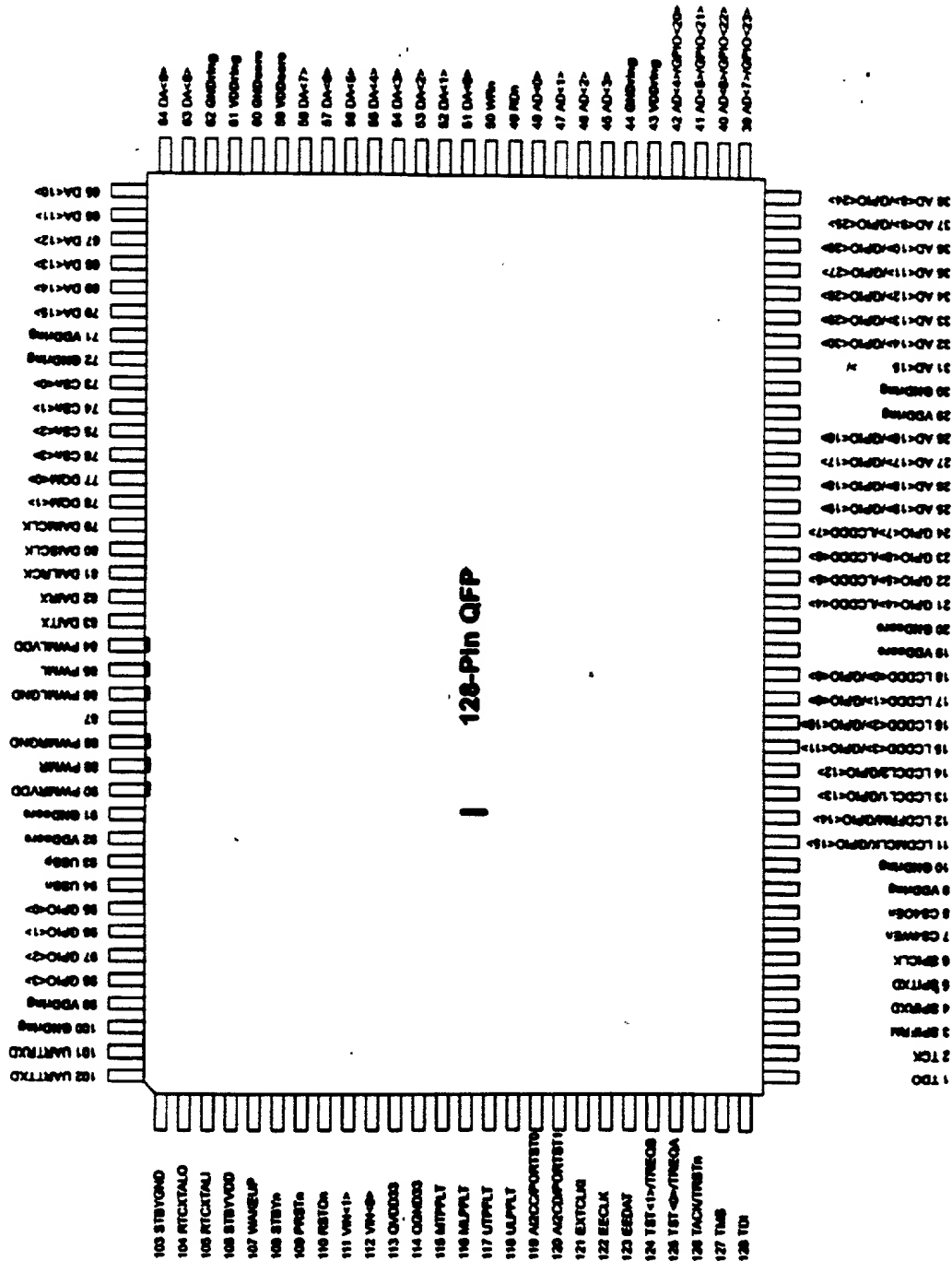


FIG 20C
Sheet 3 of 3

FIG 21



* Pins in **BOLD** are double bonded
 * Some pads are internal bonded which are not shown in this diagram. Please